

System.005

005.A

Microcomputer- Baukasten

Einplatinen- Computer
005.A/SBC

FS- Geräte- Anschluß
005.A/TVIA

Stromversorgung
005./PWR

ROM-
kassette
005.ROMC

HEX
Display Input
005./HDI

Speicher- E/A-
Erweiterung
005./SE

PROM- Programmier-
platine 005./PROMP

Steuer-
hebel
005./35

alphanumerische
Tastatur
005./KBD

005.B

Hobby-Computer

Grundgerät
005.B1

Grundgerät
005.B2

Selektier-
stift

Farb-
modulator
005./CVM

TV-Monitore:

005./TVB Schwarz/Weiss
Bildr. 31cm

005./TVC Farbe
Bildr. 42cm

005.C

Video-Computer

Grundgeräte
005.C1
005.C2

Speicher- Erweiterung
005.C/SE

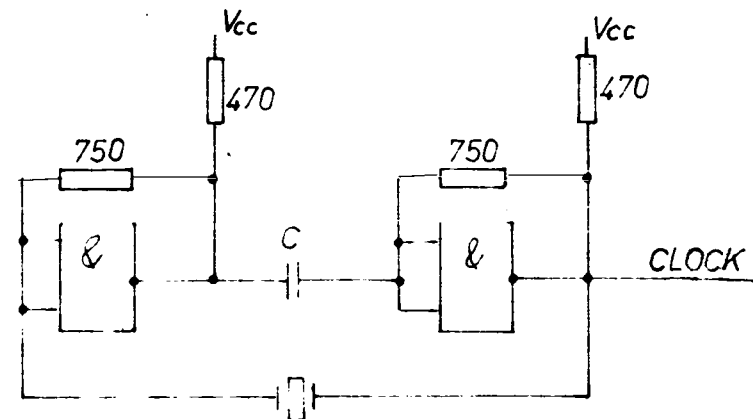
Kassetten-
Laufwerk
005./ACR

Darstellung unmaßstäblich!

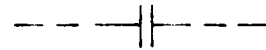
Fig.1

ca 2.70

Taktgenerator



Baustufe:



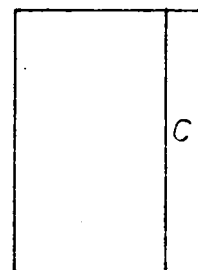
2-S007

C: bei Umsatzsteuerung 1,5 μ
ansonsten je nach Frequenz

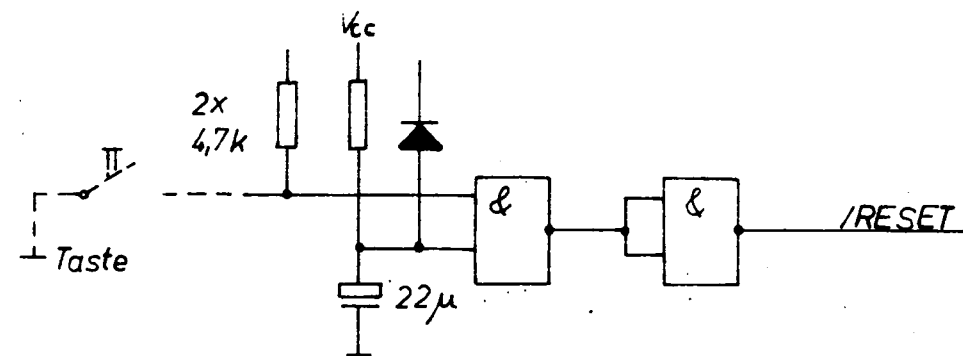
Schaltkreis: D 201 bis 14 MHz,
darüber 74 S01

Die beiden im Chip noch freien Gatter
eingangsseitig mit Masse beschalten!

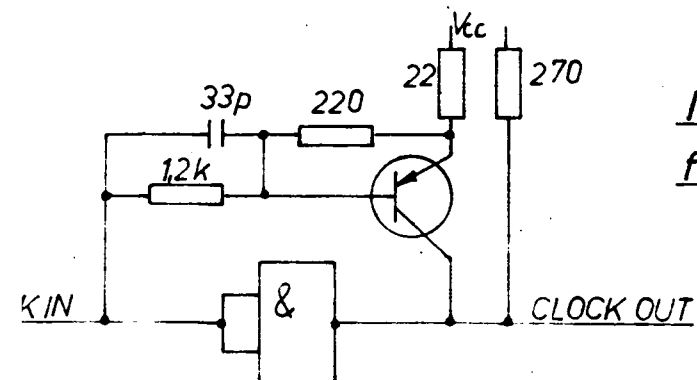
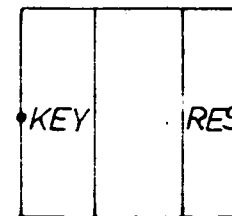
Symbol



Rücksetzsignal Bildung



Symbol



Impulsformung
für U 880-Takt

Symbol

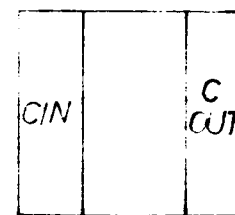
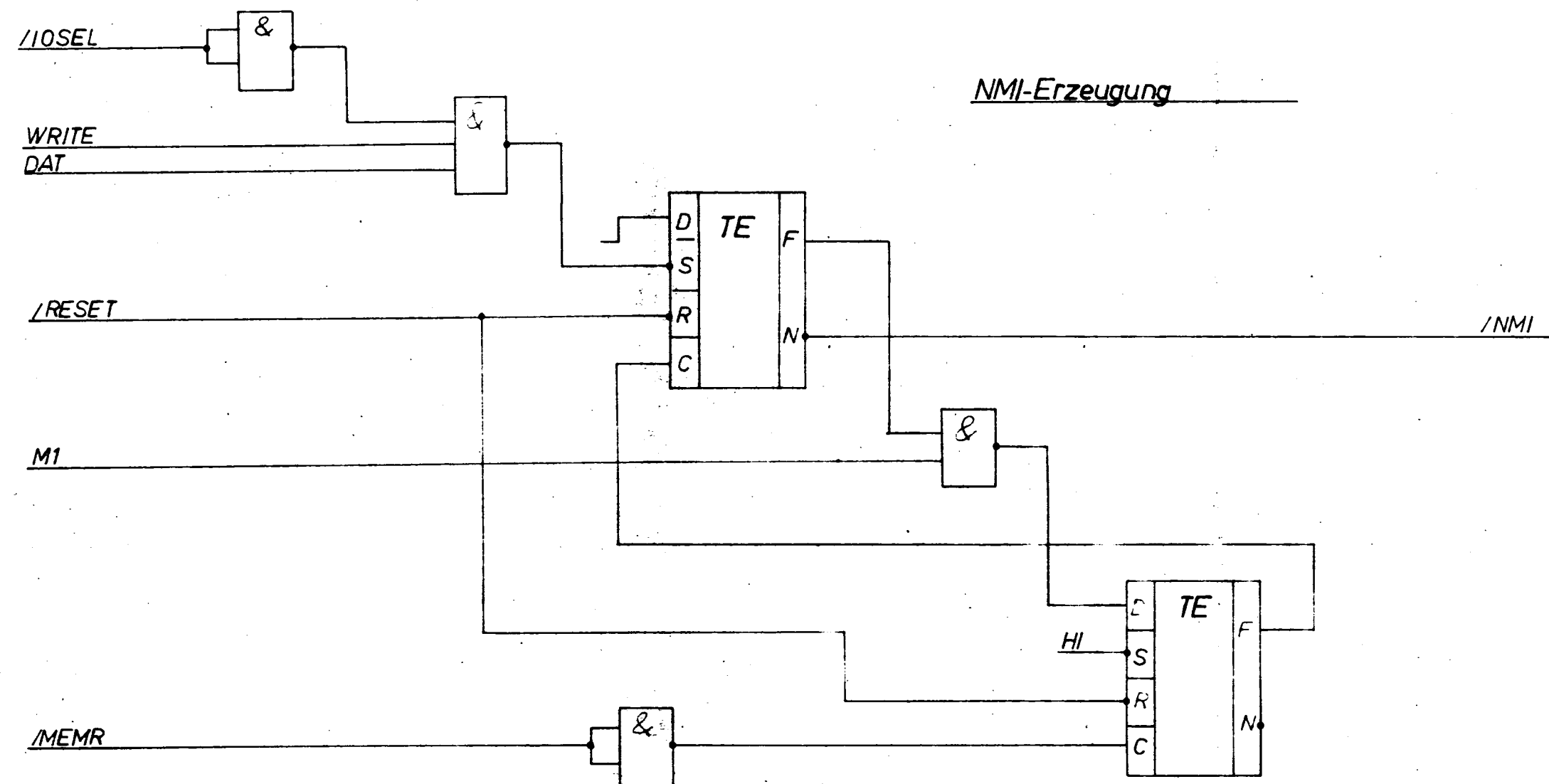


Fig.2



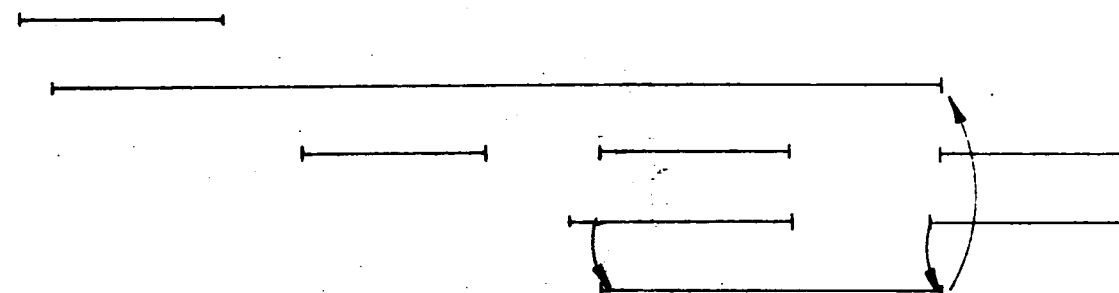
OUT-Befehl

NMI

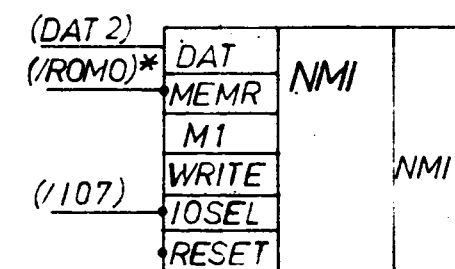
MEM. REQ

M1

TRIGGER II

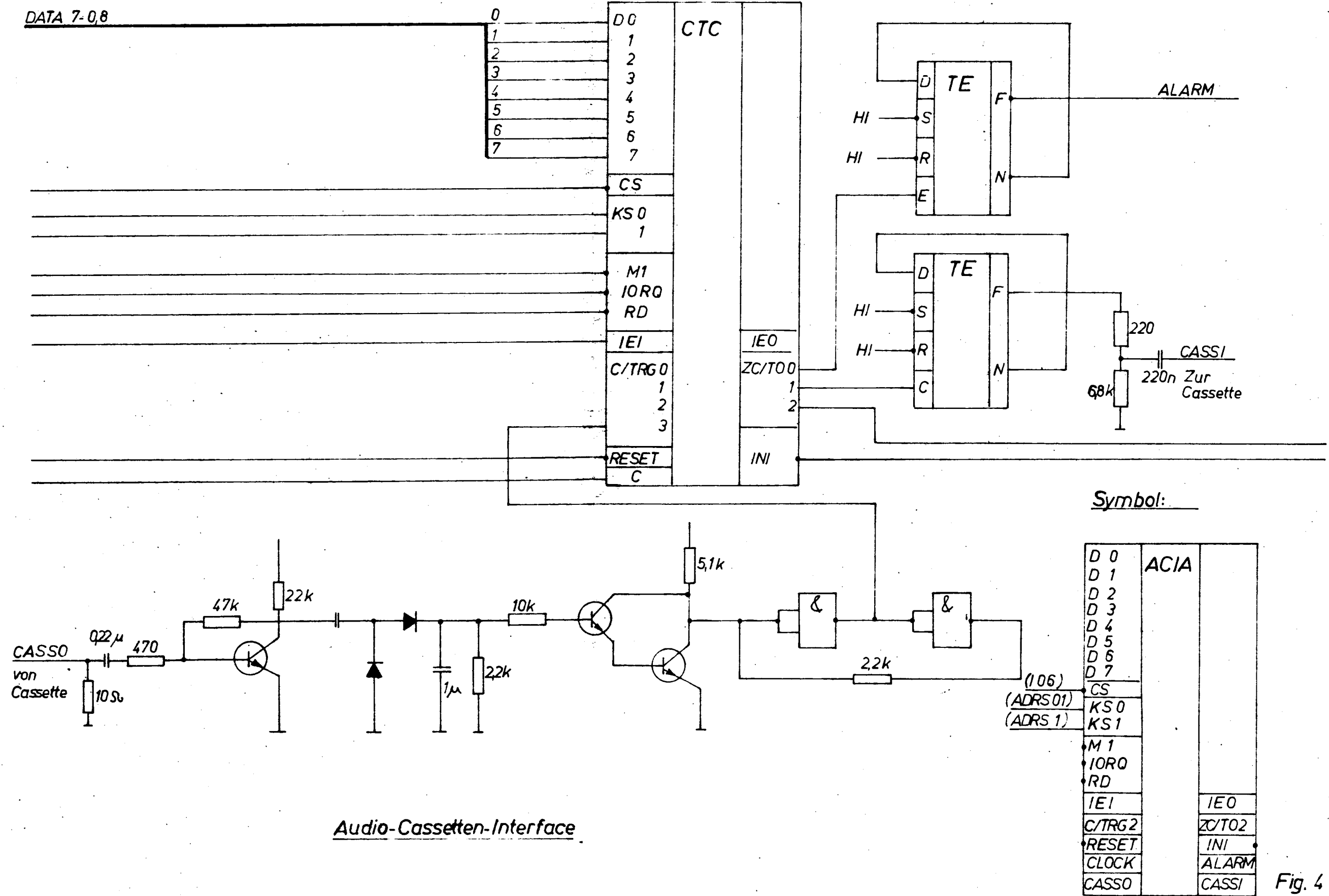


Symbol



*NMI führt zu Programmstart
von Adresse X0066'.
Damit wird stets ROM 0
adressiert.

Fig. 3



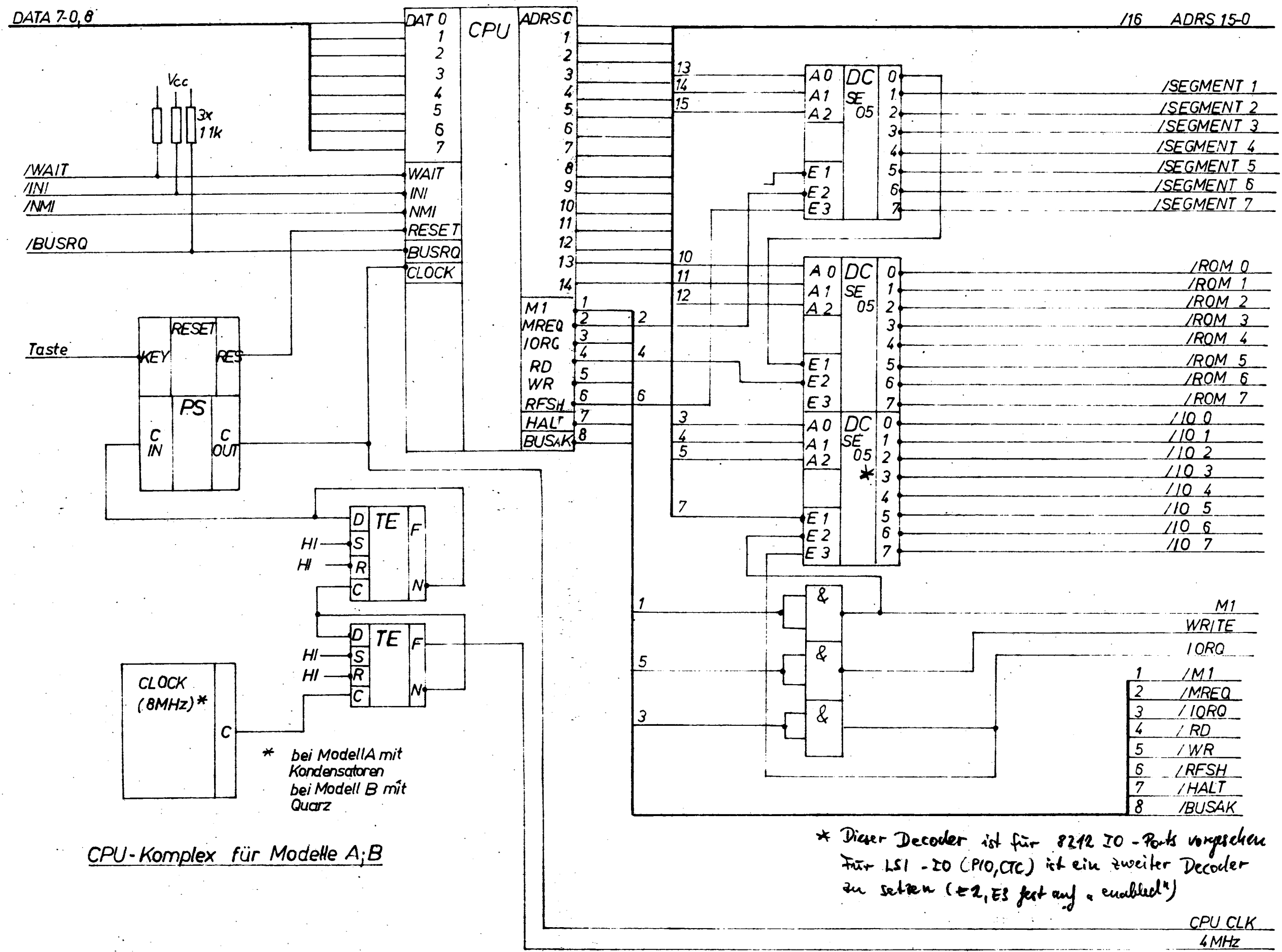
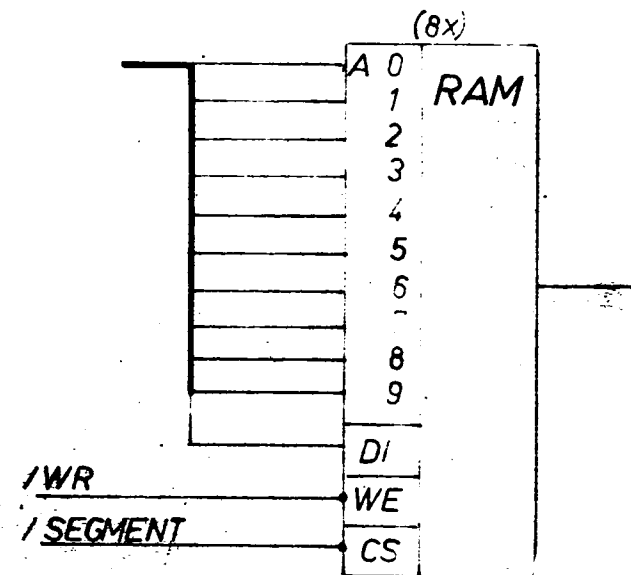
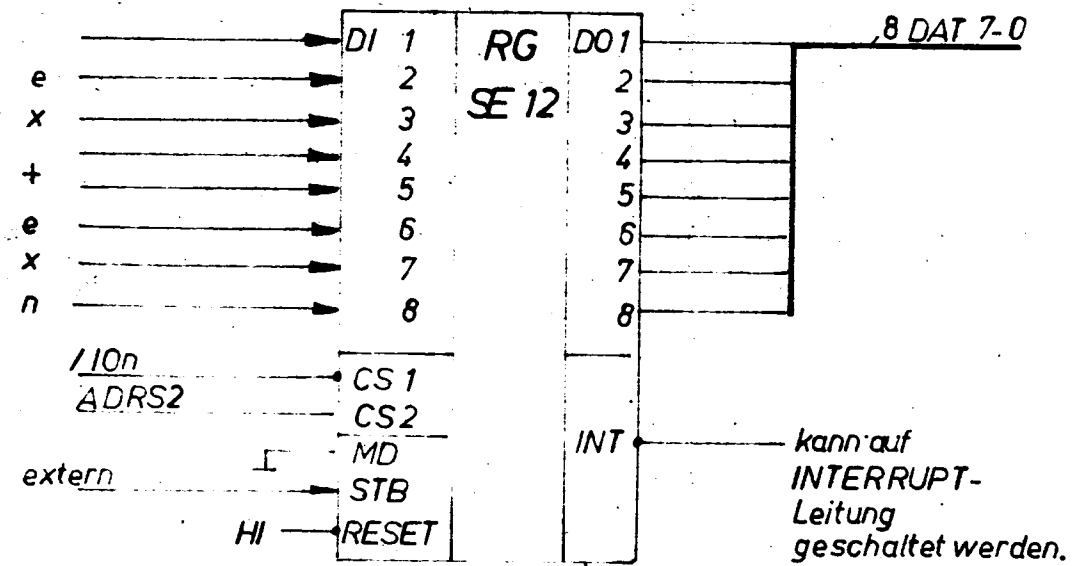


Fig.5

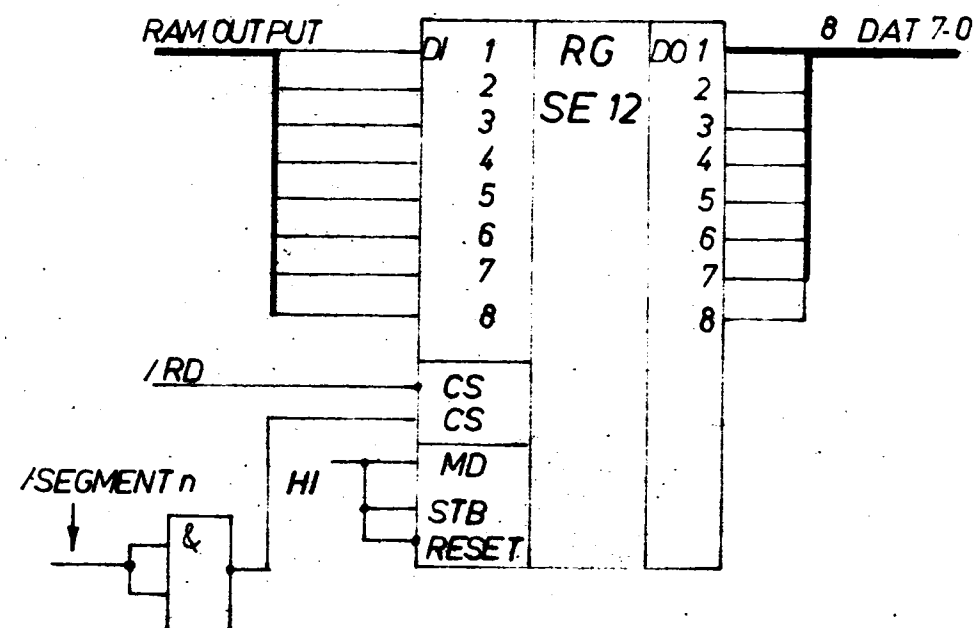
Detail RAM-Array



Detail 8212 INPUT



Detail RAM-Ausgangspuffer



Detail 8212 OUTPUT

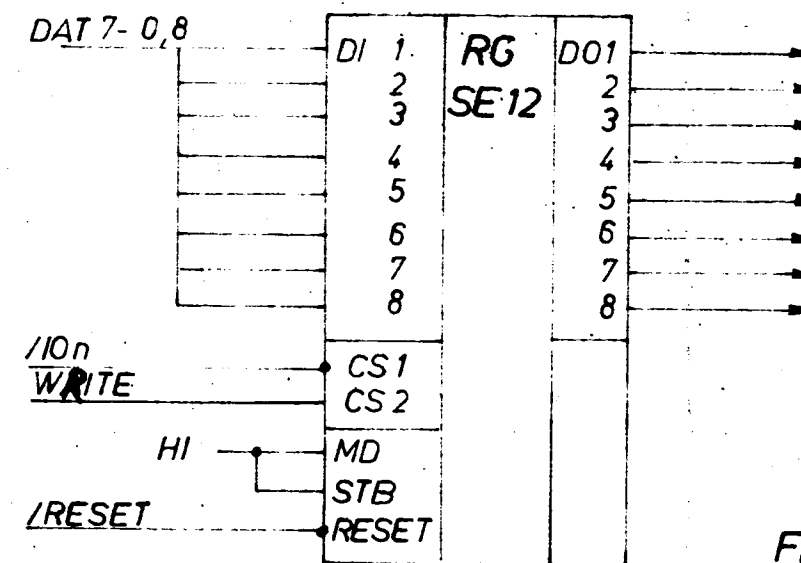
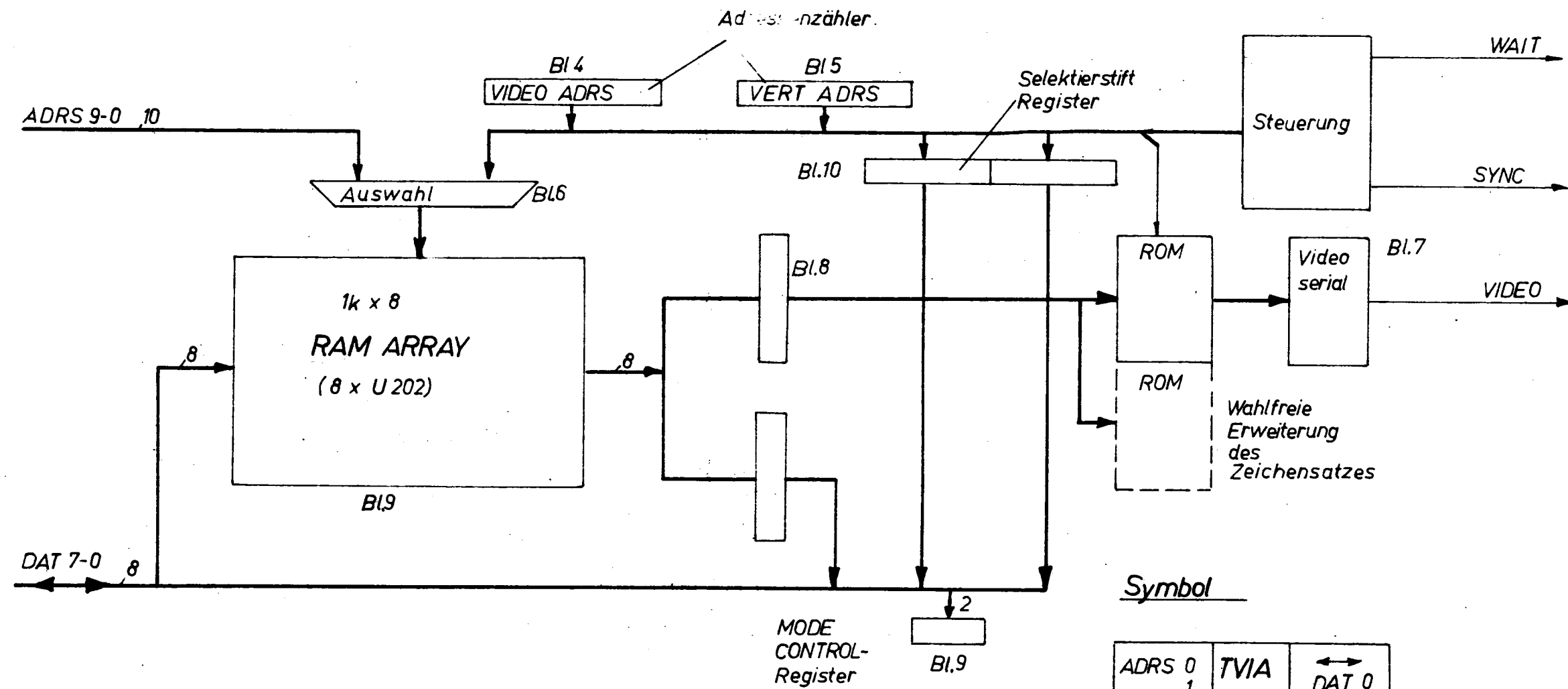


Fig. 6



Symbol

ADRS 0	TVIA	DAT 0
1		1
2		2
3		3
4		4
5		5
6		6
7		7
8		INI
9		WAIT
(/SEGMENT1)	DVCSEL	
(/IO 0)	IOSEL 0	
(/IO 1)	IOSEL 1	
(/IO 7)	IOSEL 2	
	RD	
	WRITE	
	SELPEN	VIDEO
4MHz	CLOCK	SYNC

Fig.7, BI 0

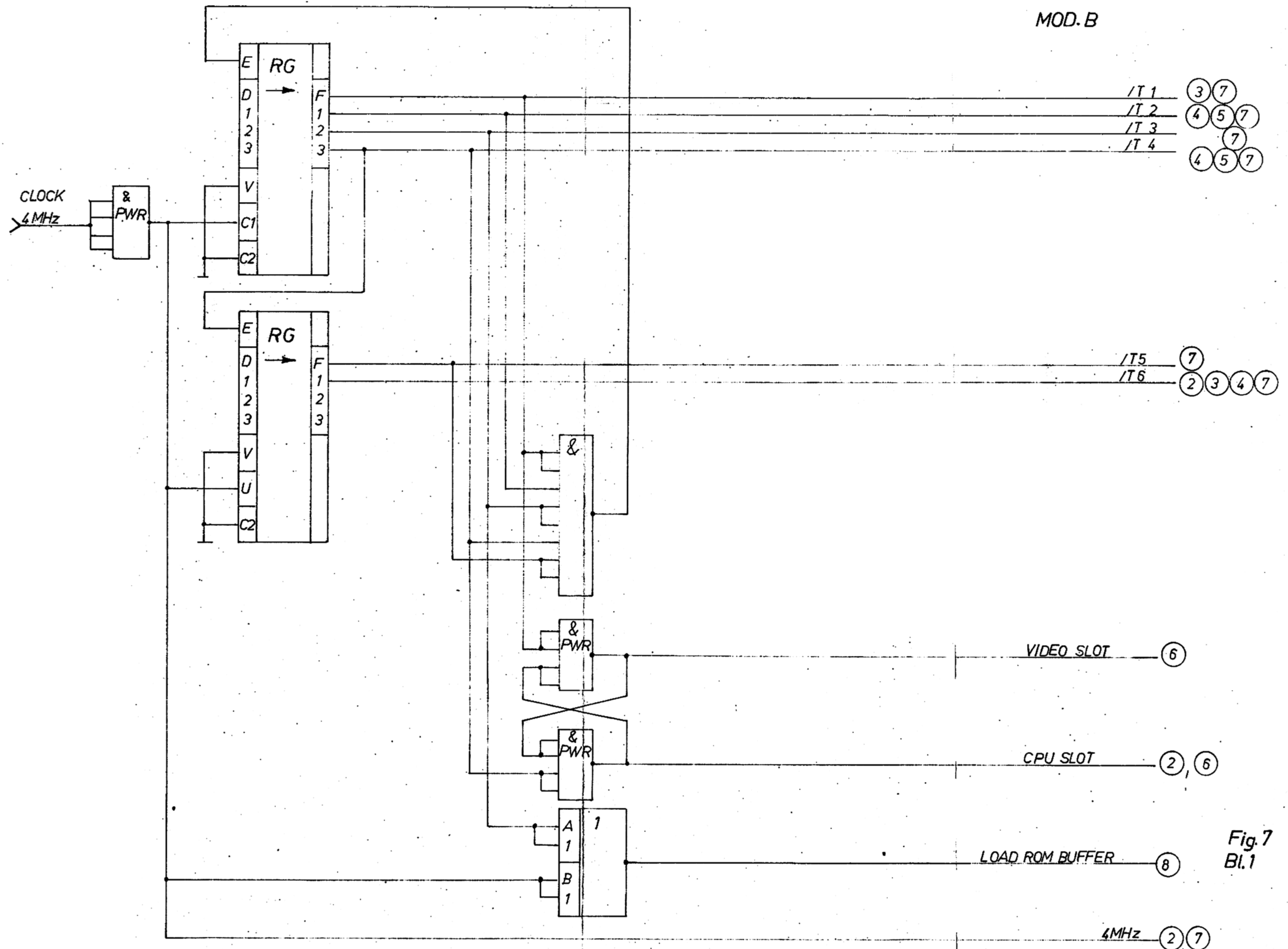


Fig. 7
Bl. 1

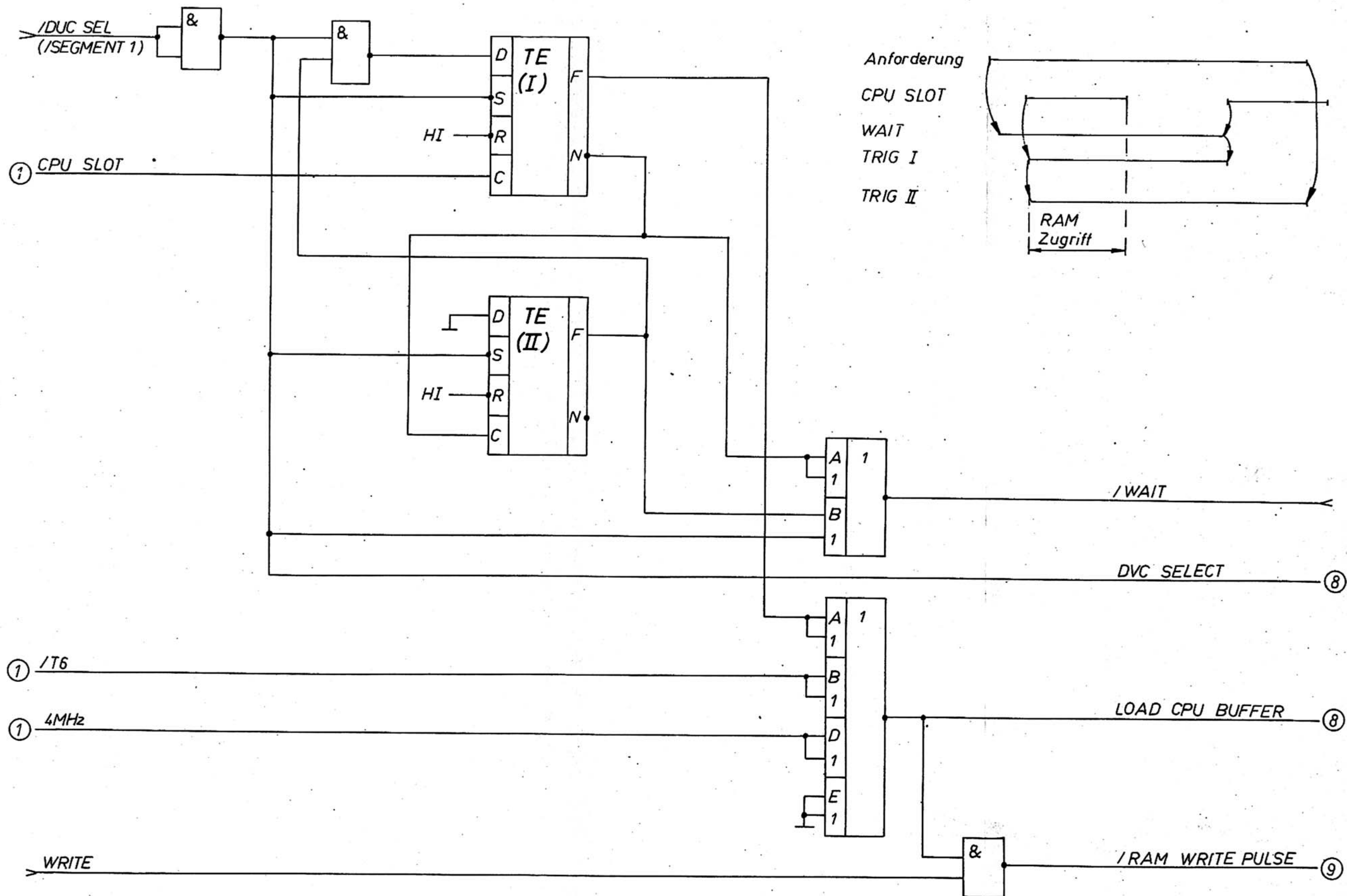


Fig.7, Bl.2

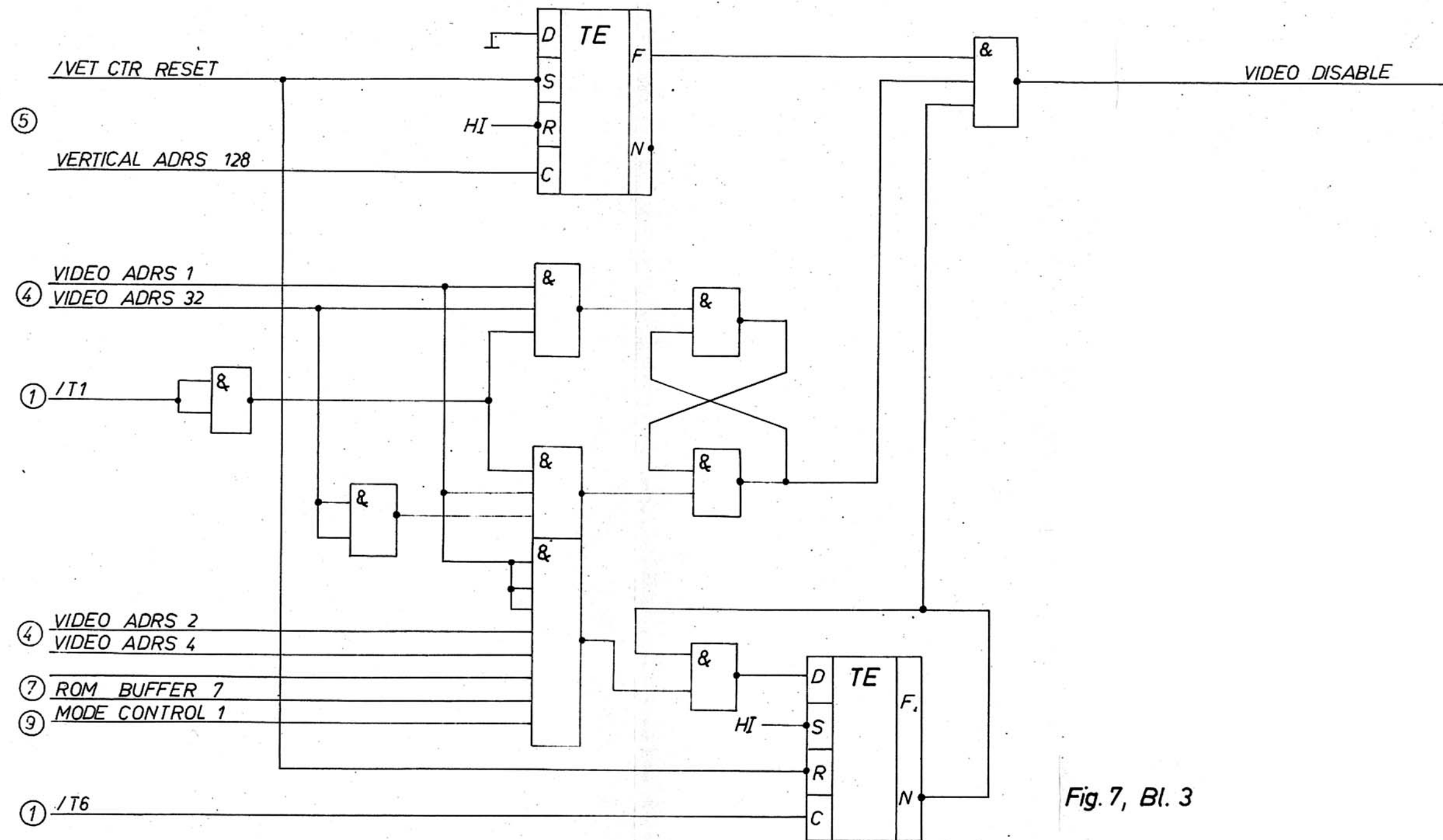


Fig. 7, Bl. 3

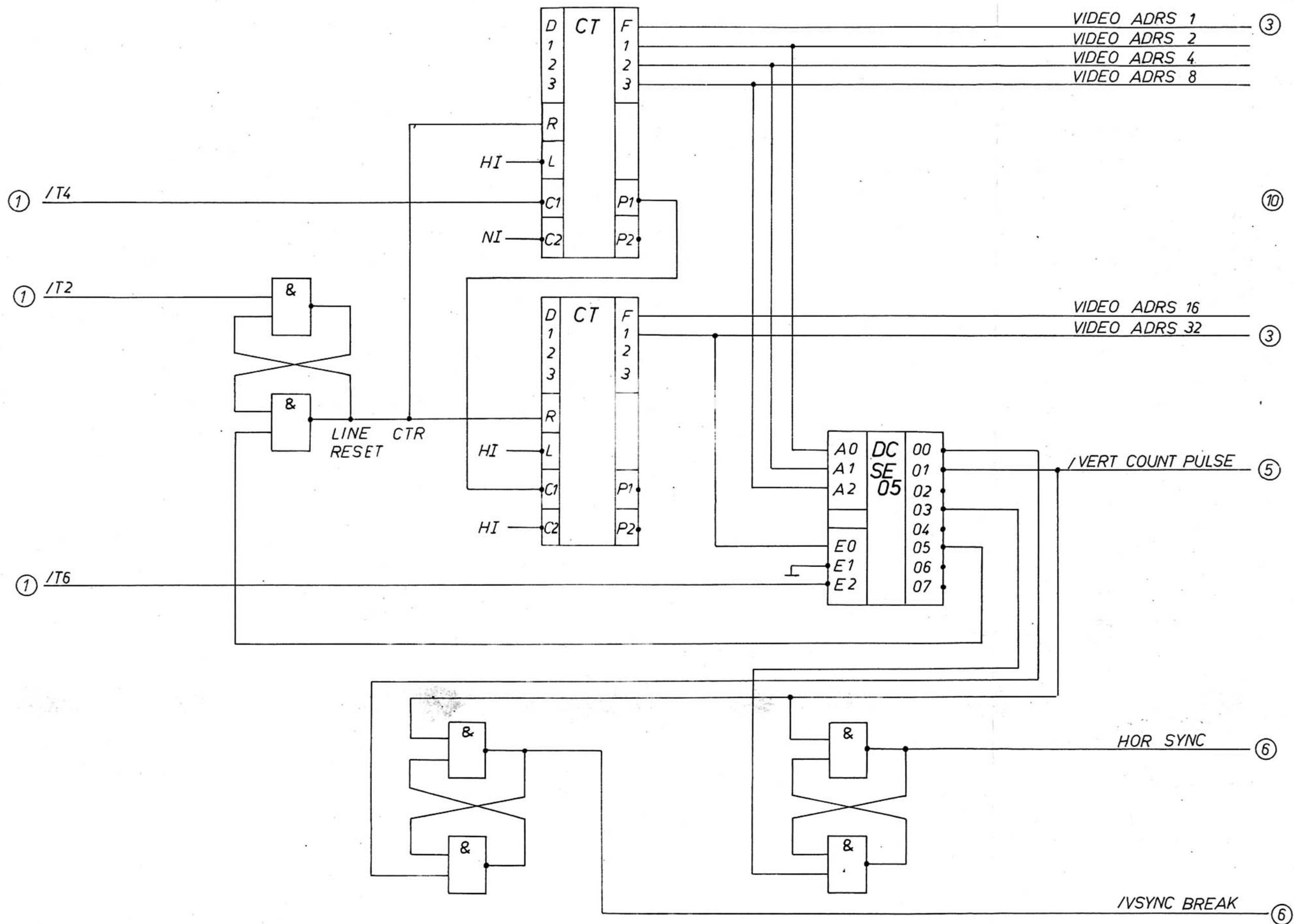


Fig.7, Bl.4

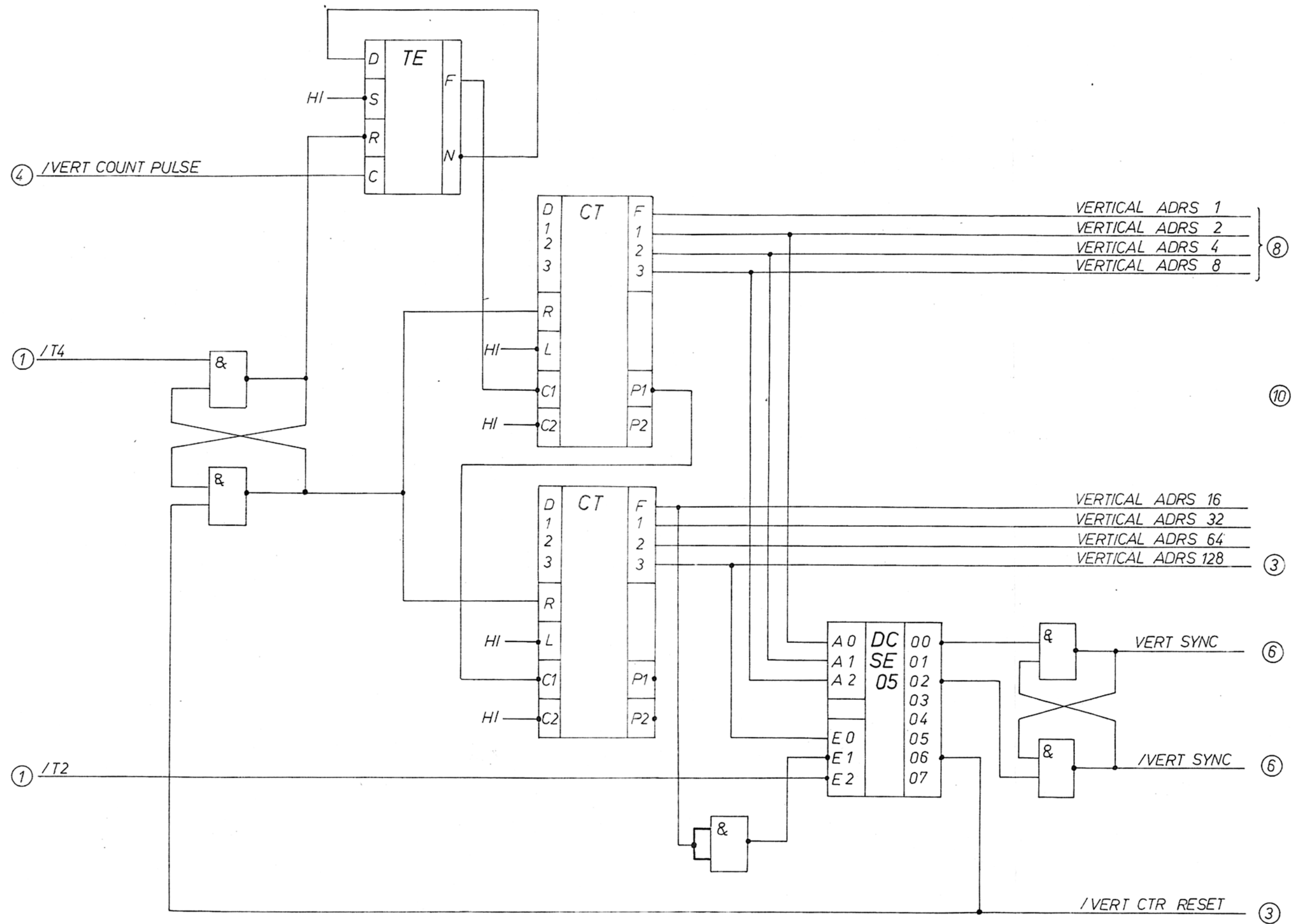
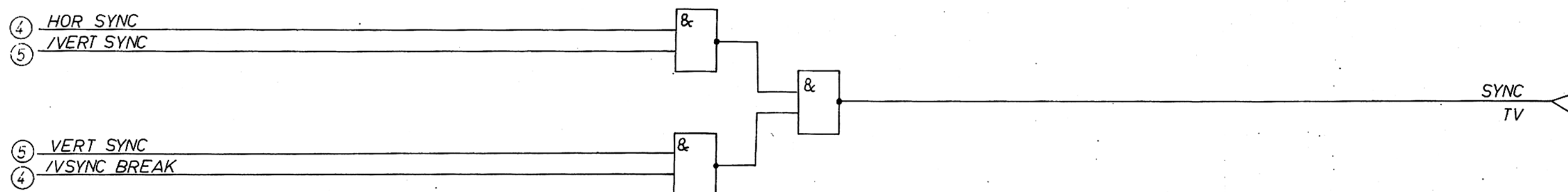


Fig.7, Bl.5



Prinzip der Adressen - Auswahl für RAM

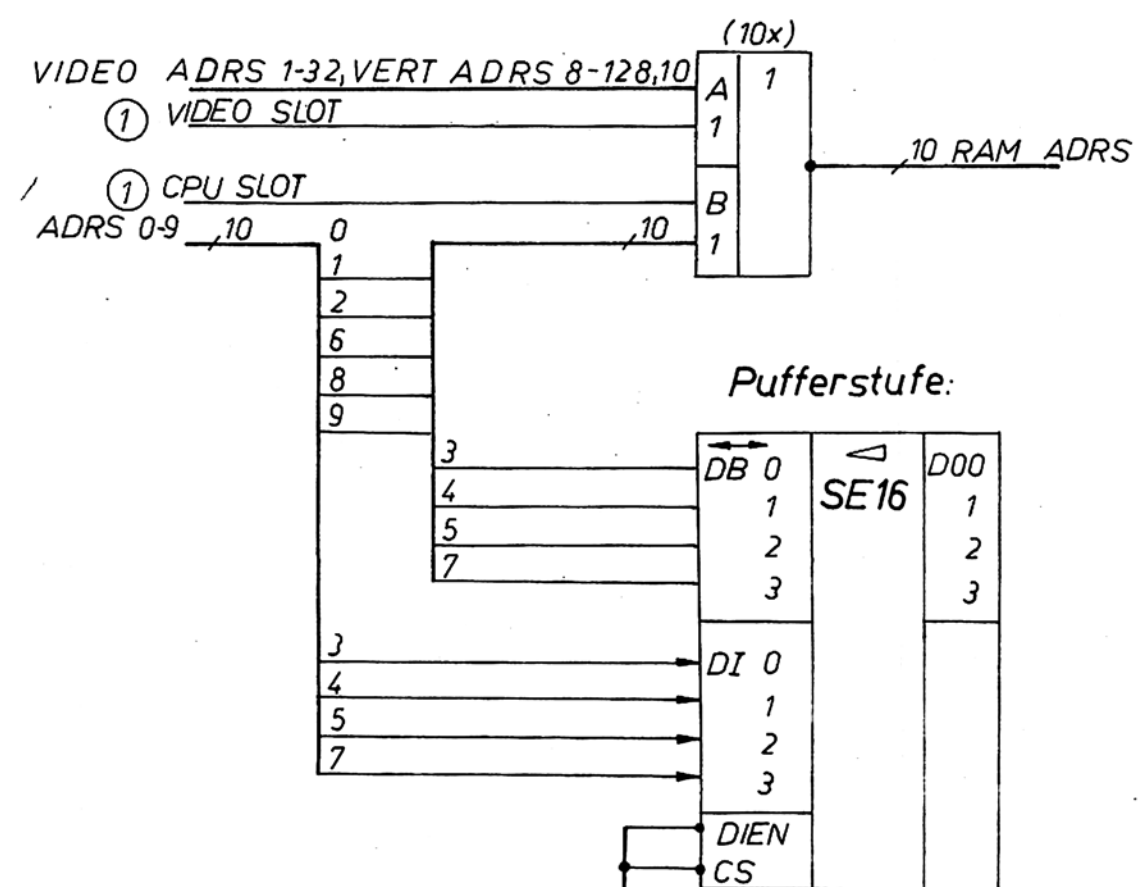


Fig. 7, Bl. 6

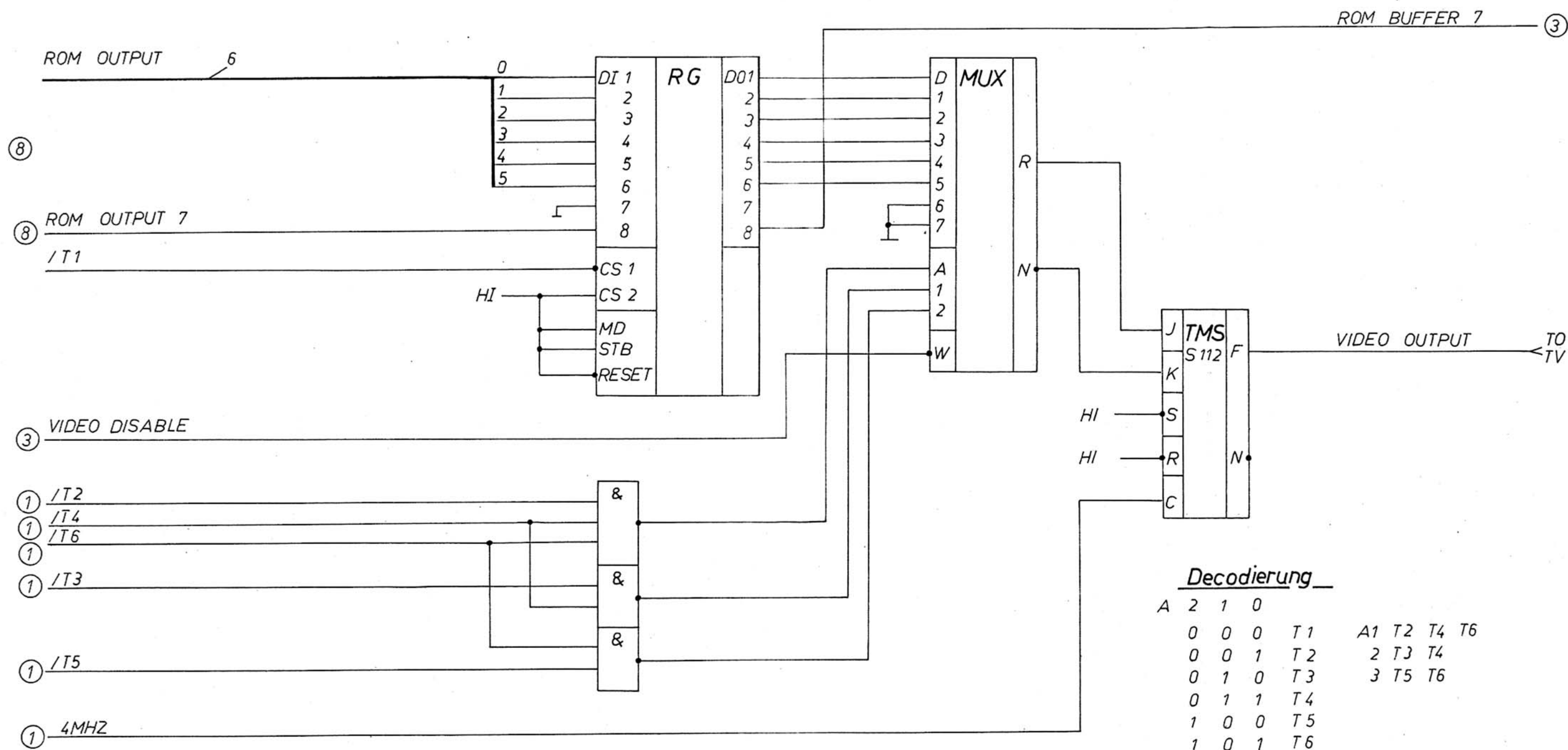
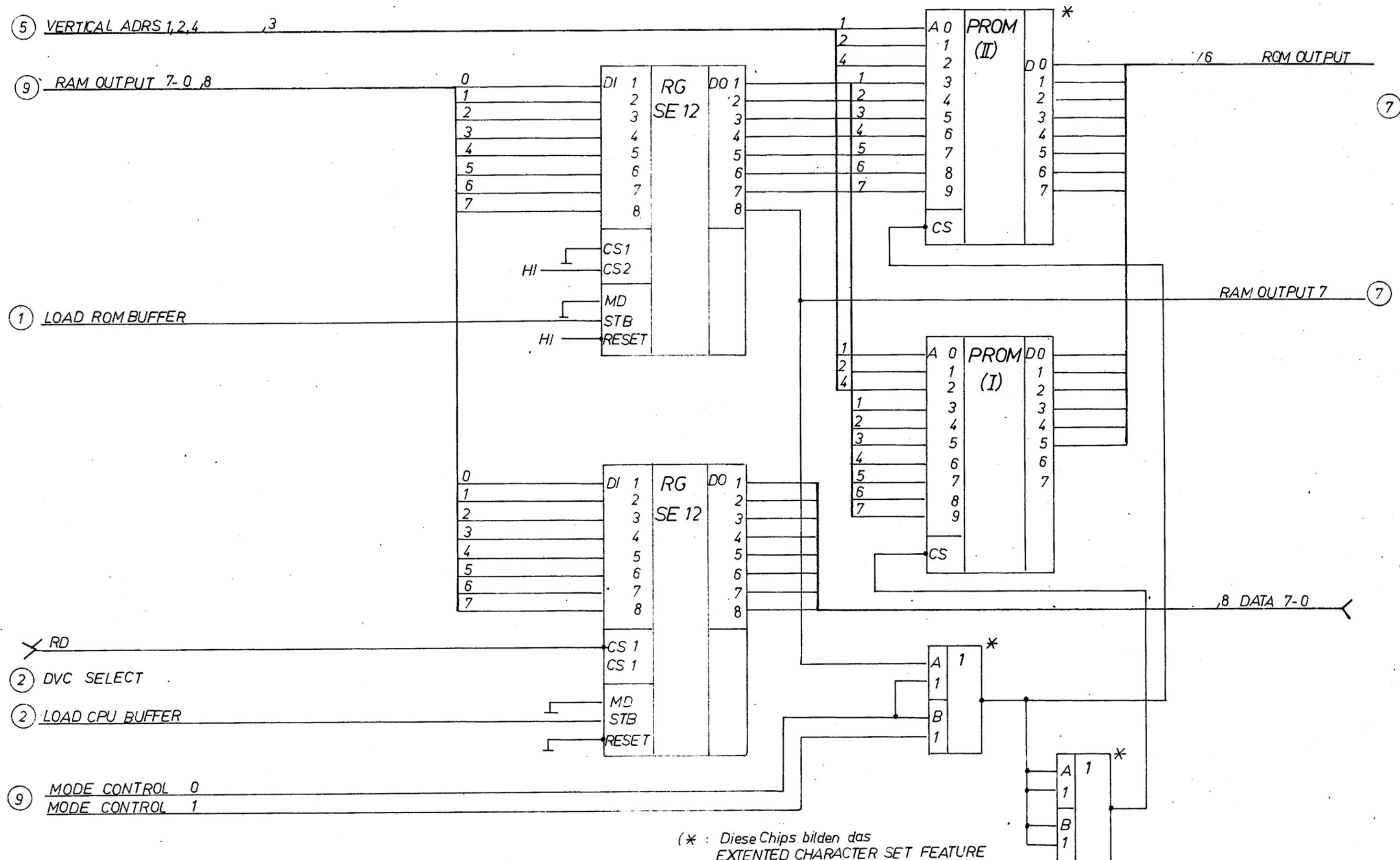


Fig. 7, Bl. 7



(* : Diese Chips bilden das
EXTENDED CHARACTER SET FEATURE
(Erweiterung des Zeichensatzes).
Sie sind in der Grundversion nicht
bestückt.

Fig.7, Bl.8

Detail RAM-Array

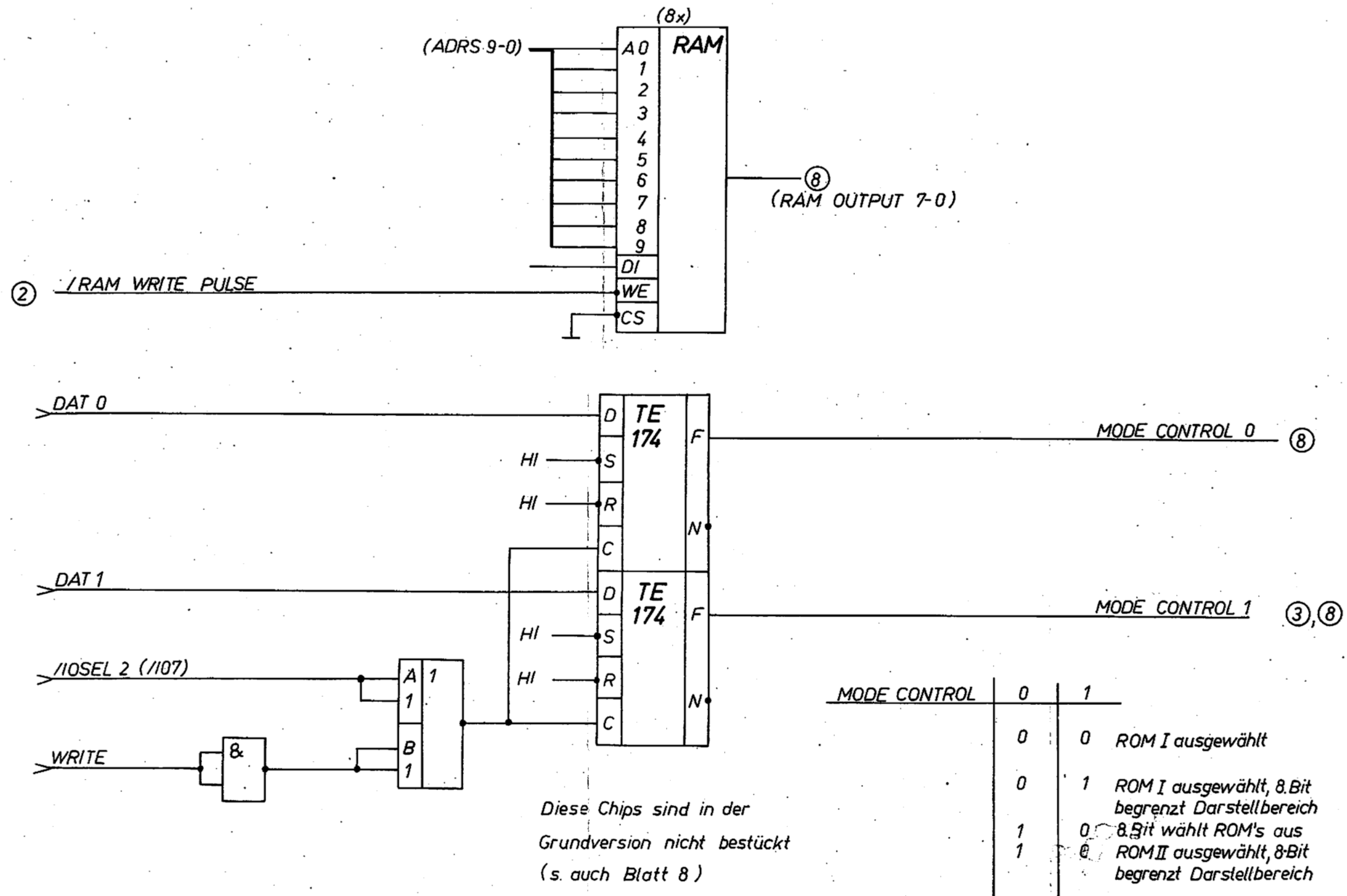


Fig. 7, Bl. 9

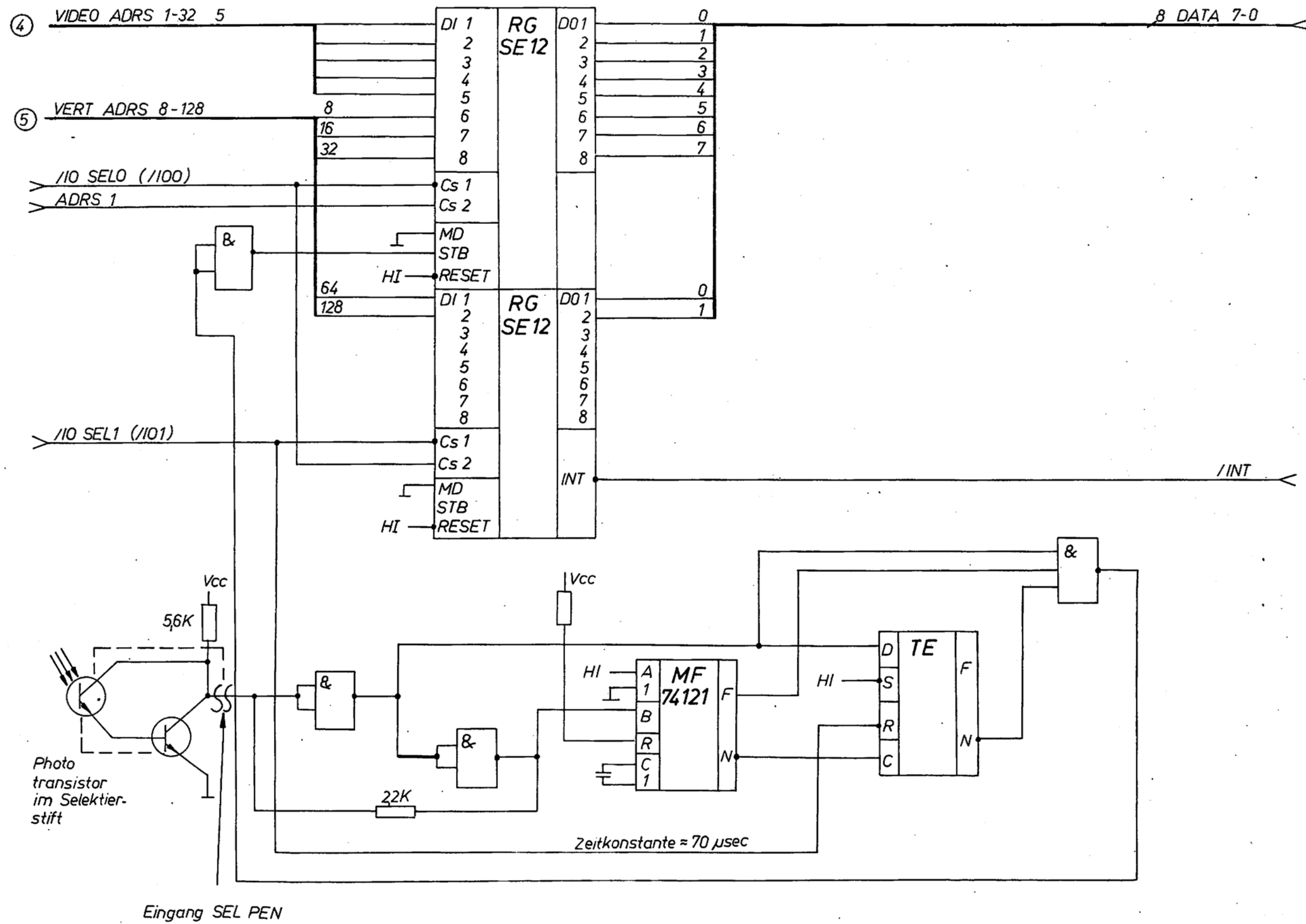
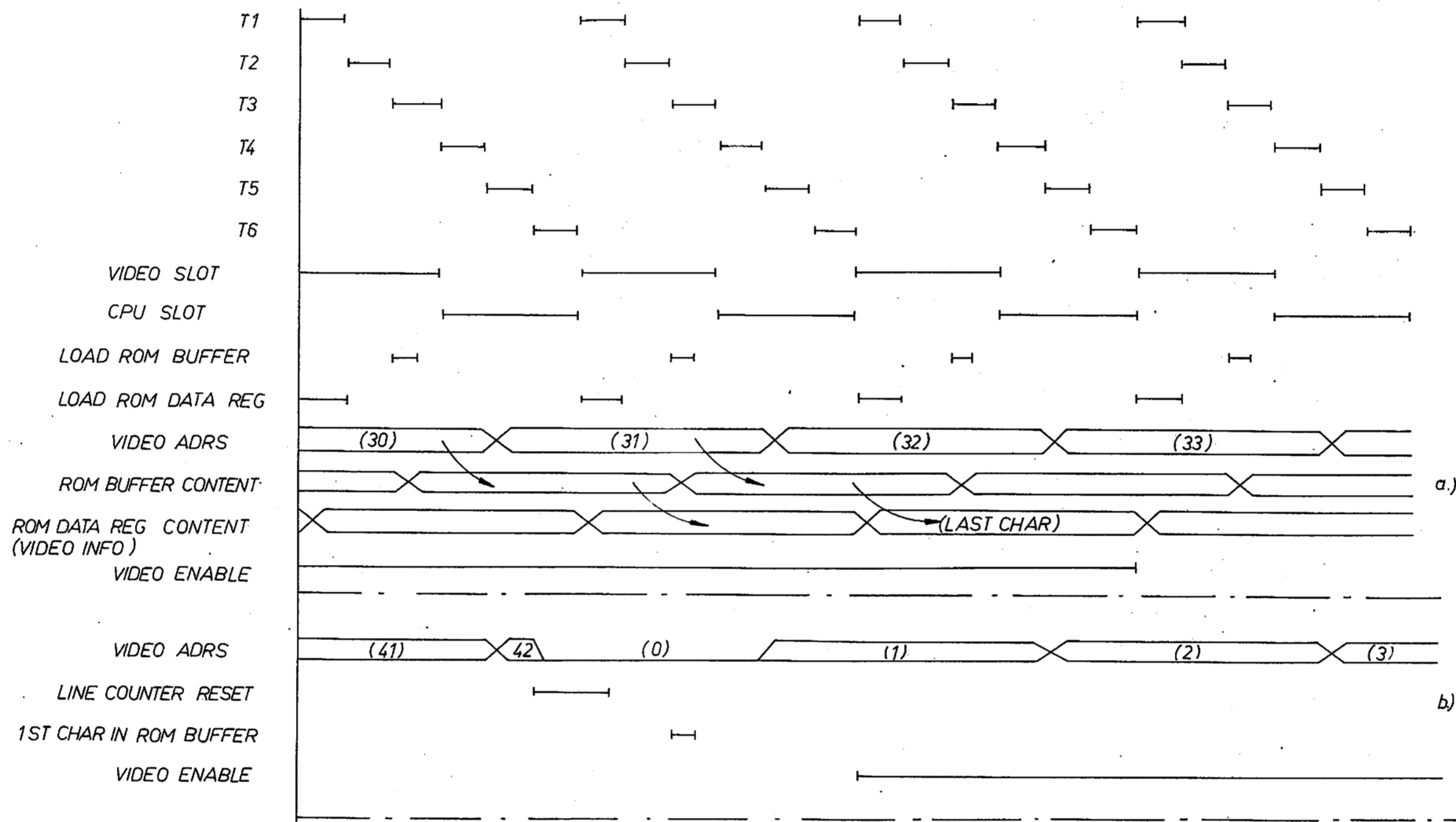


Fig.7, Bl.10



a): Zeilenende b): Zeilenanfang

Zeilenaufteilung:

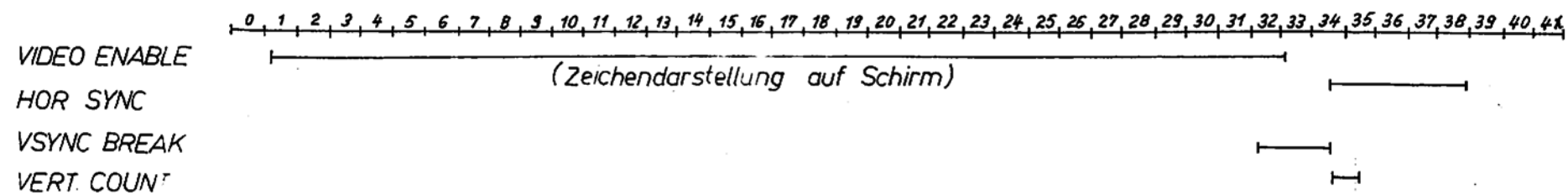


Fig. 7, Bl. 11

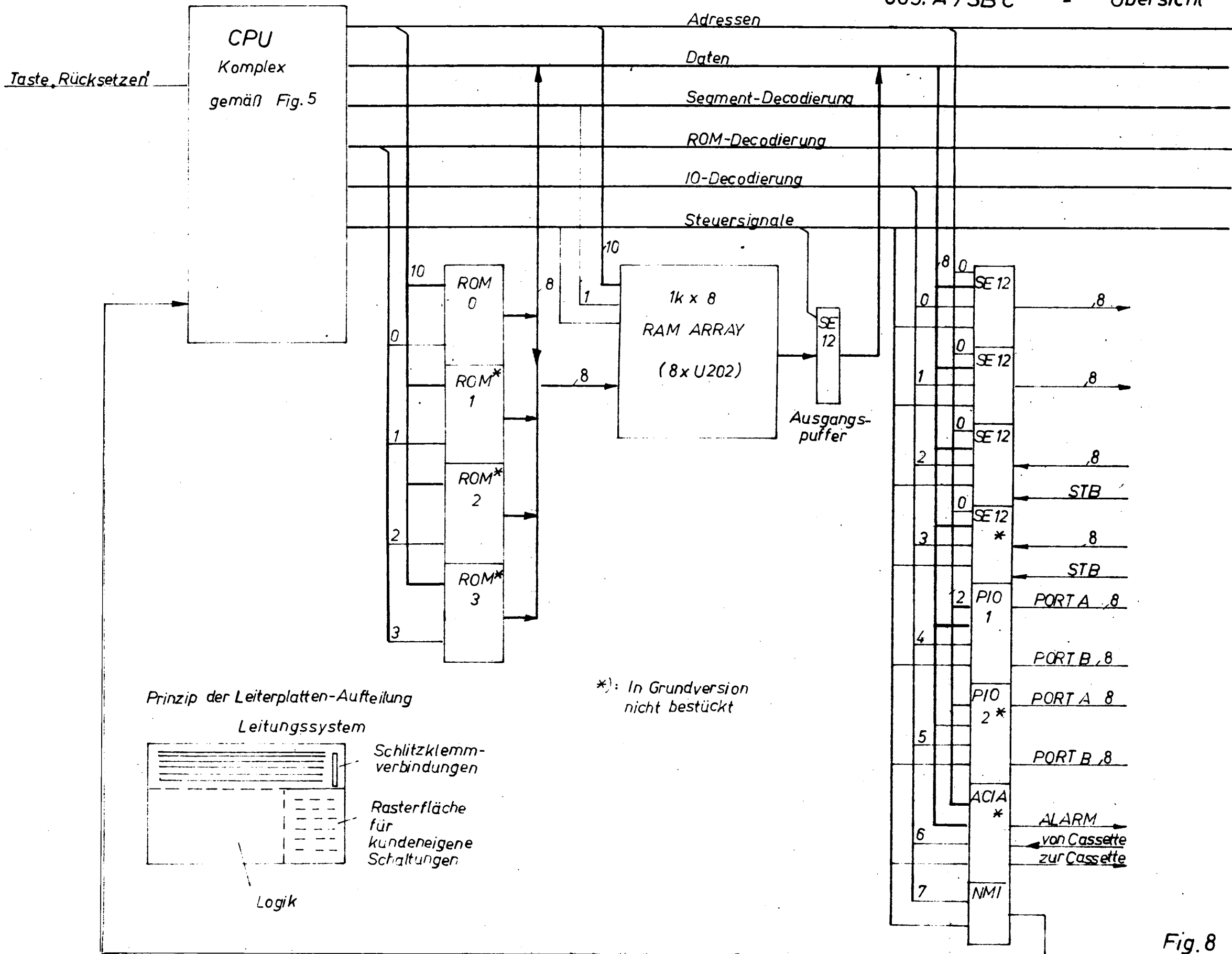


Fig. 8

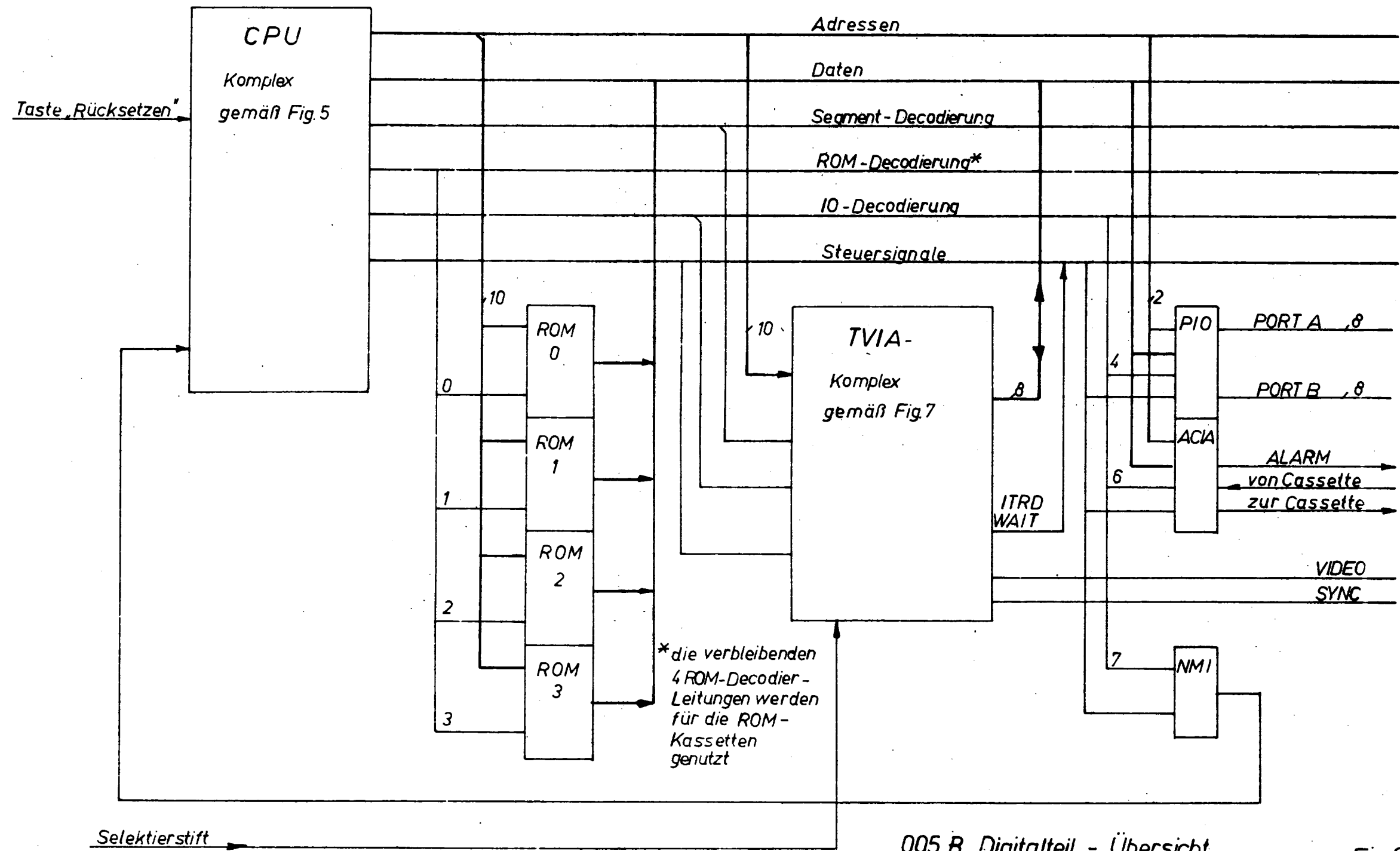
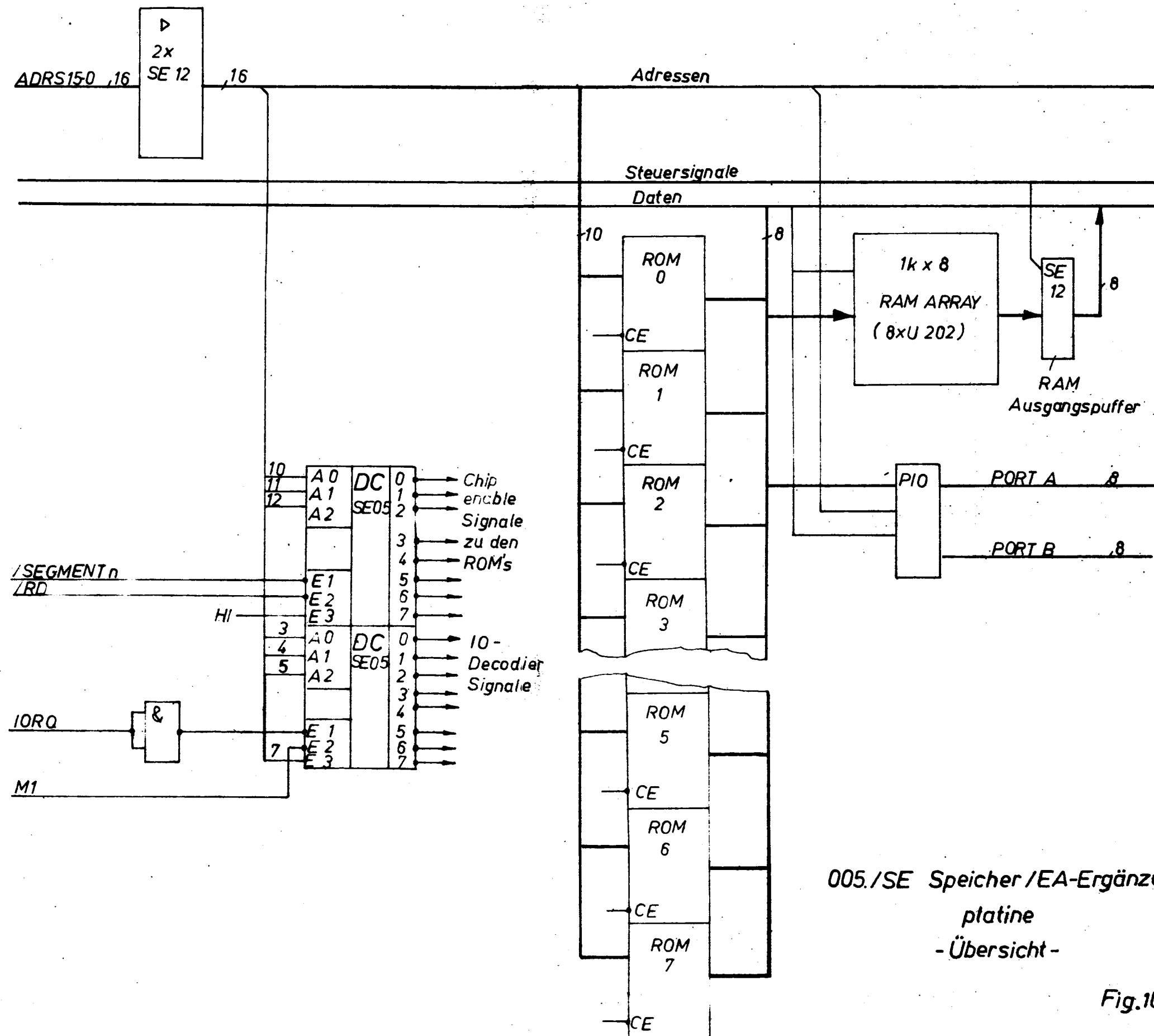
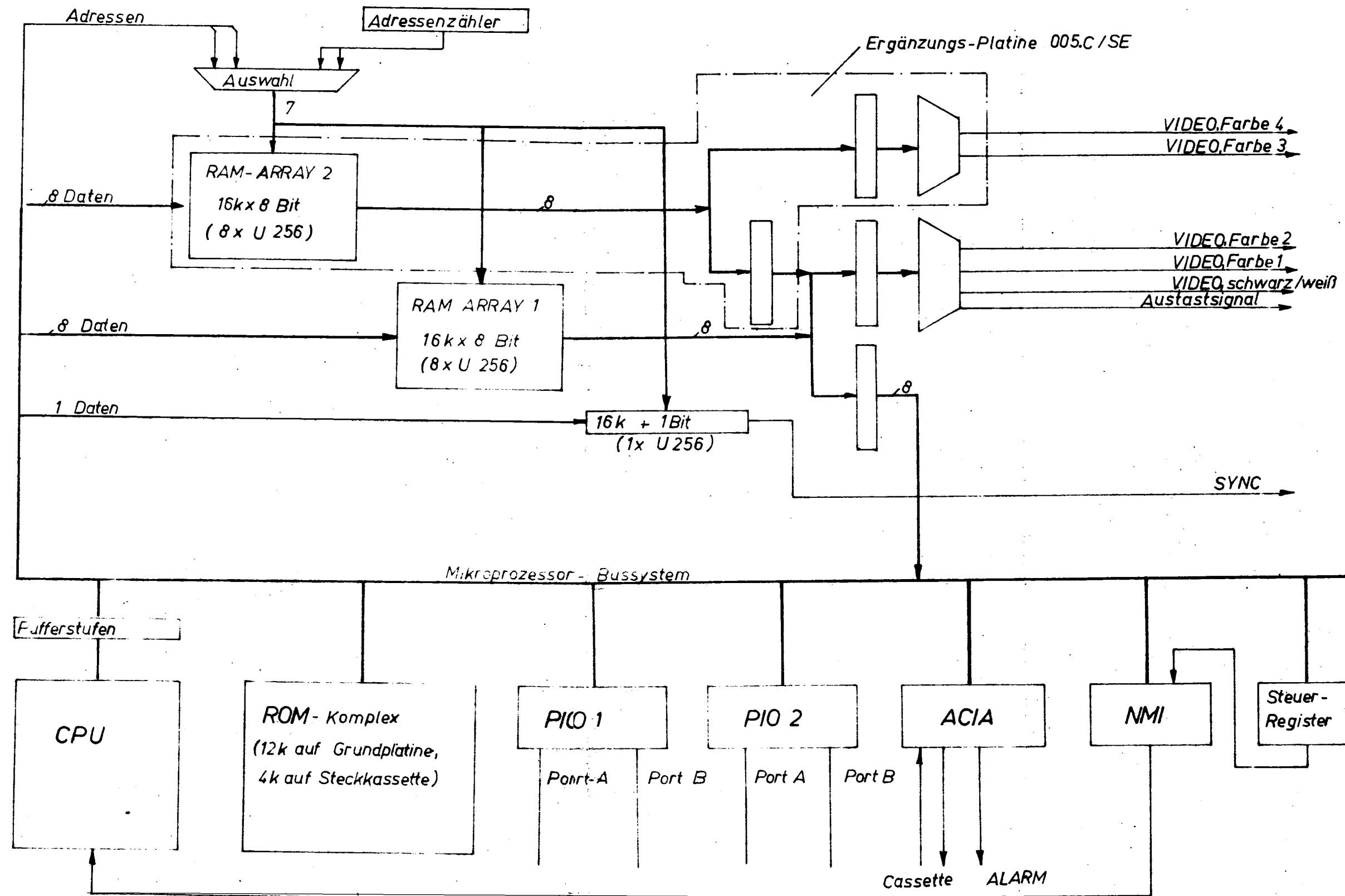


Fig.9

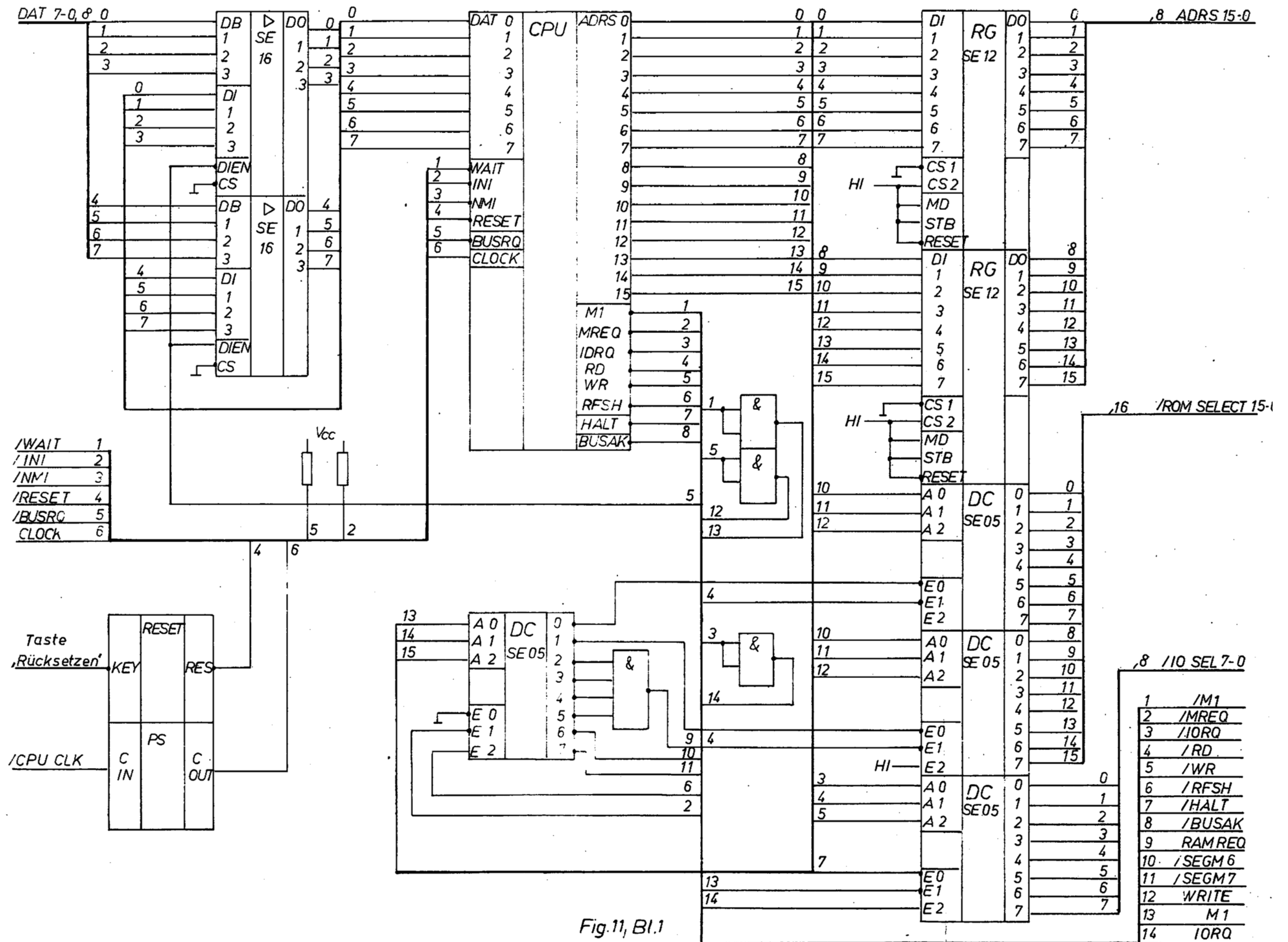


005./SE Speicher/EA-Ergänzungs-platine
- Übersicht -

Fig.10



Modell 005.C Digitalteil - Blockschaltbild Fig.11, Bl.0



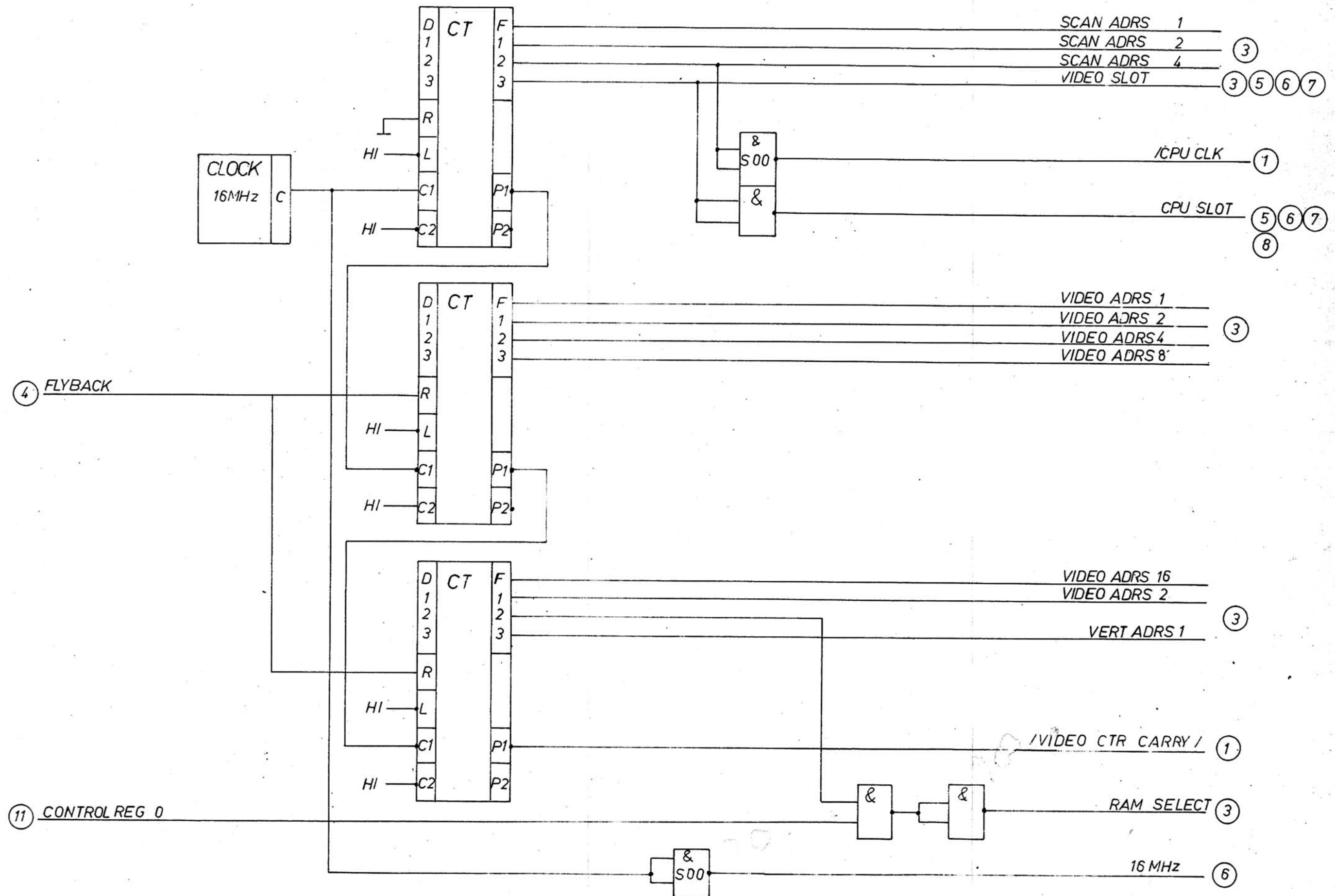


Fig. 11, Bl. 2

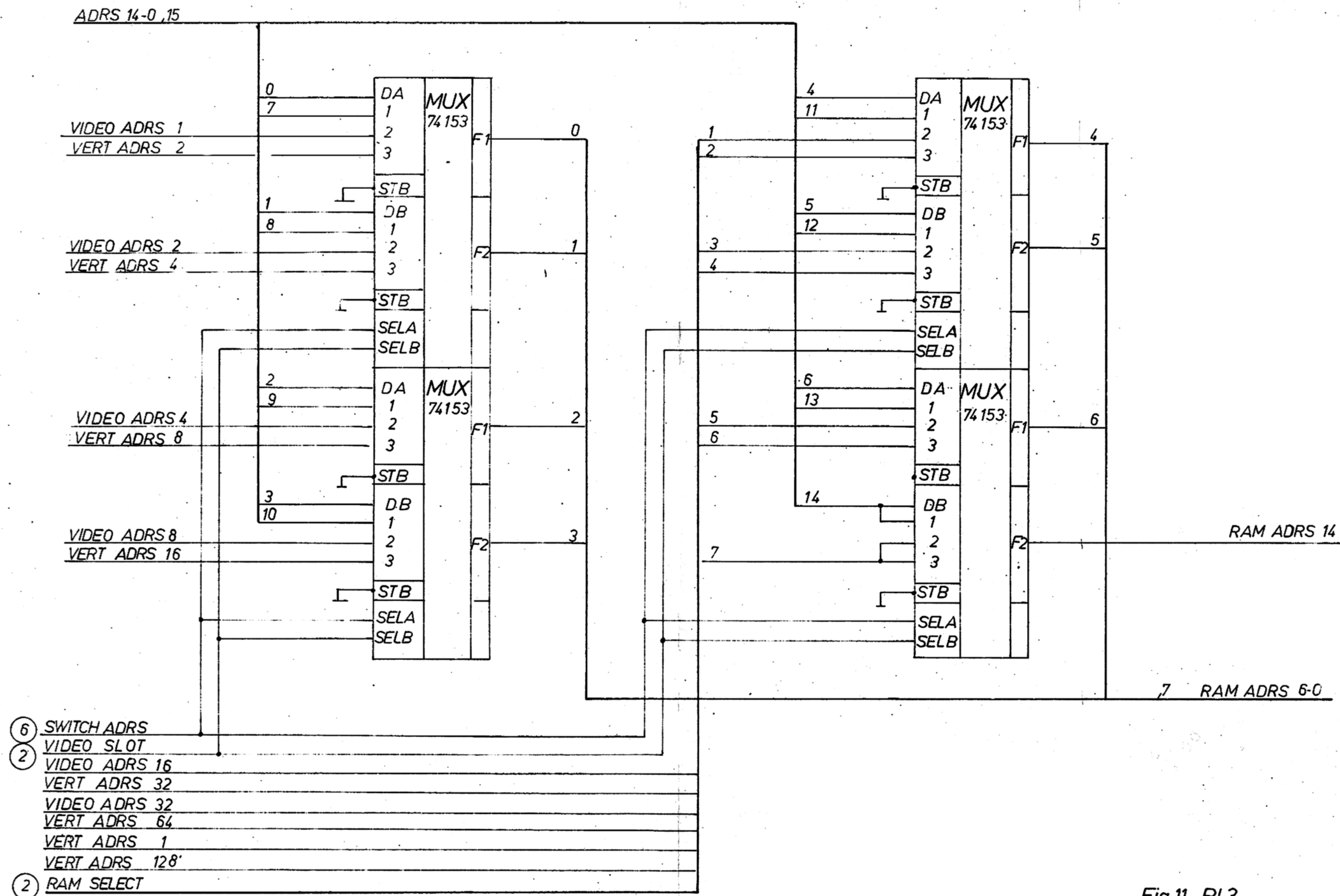


Fig.11, Bl.3

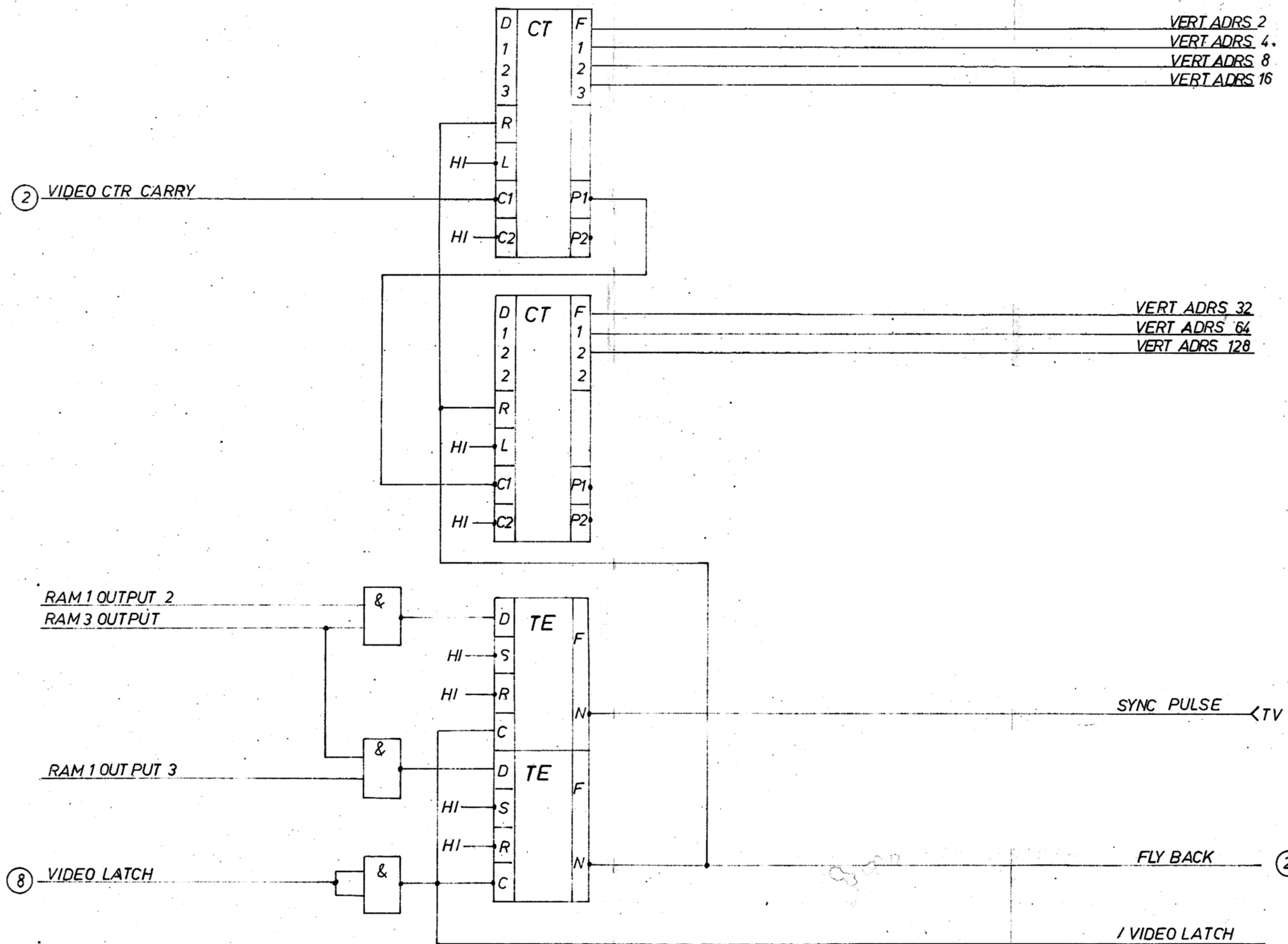


Fig.11,B14

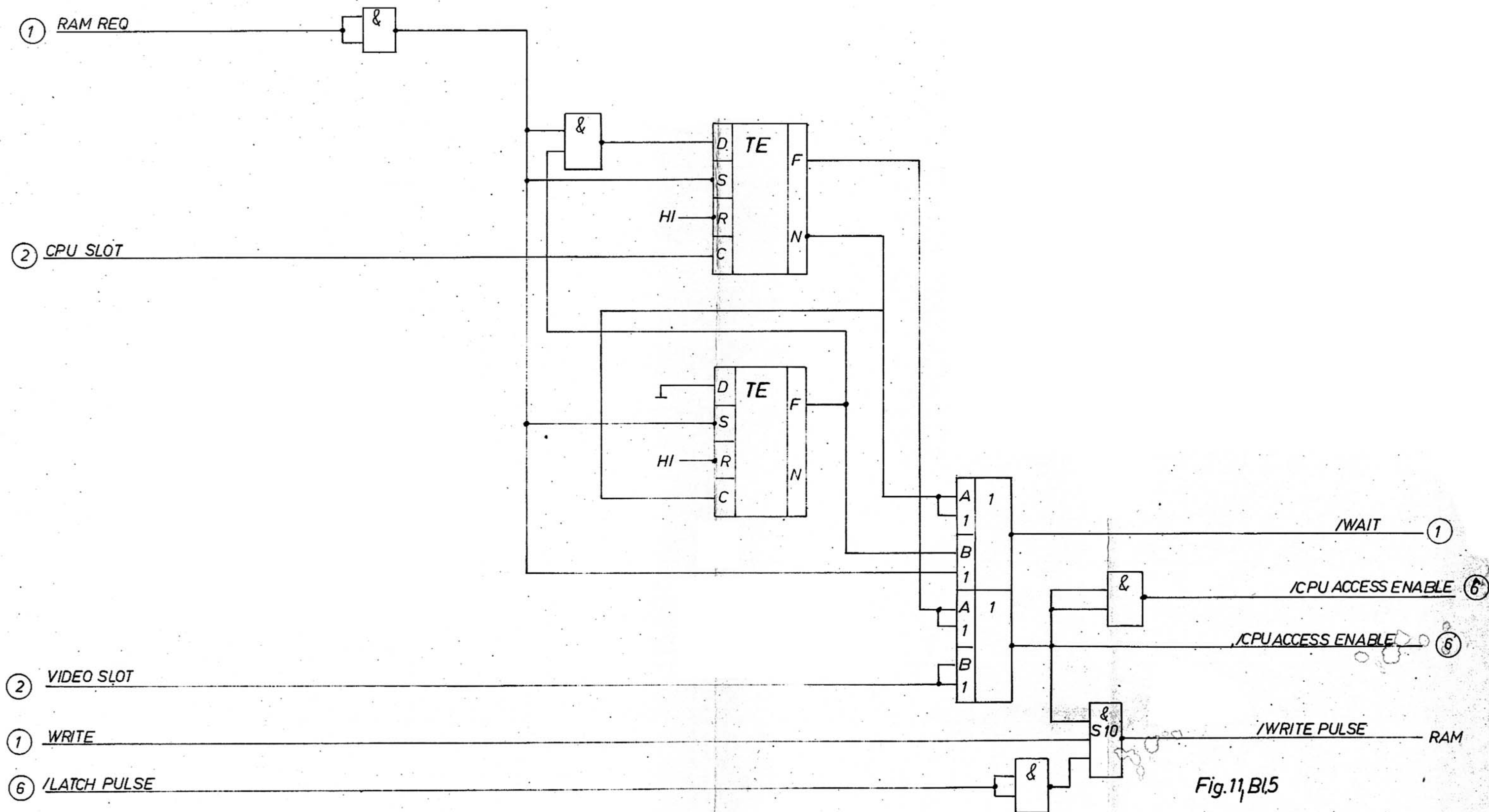


Fig.11, Bl.5

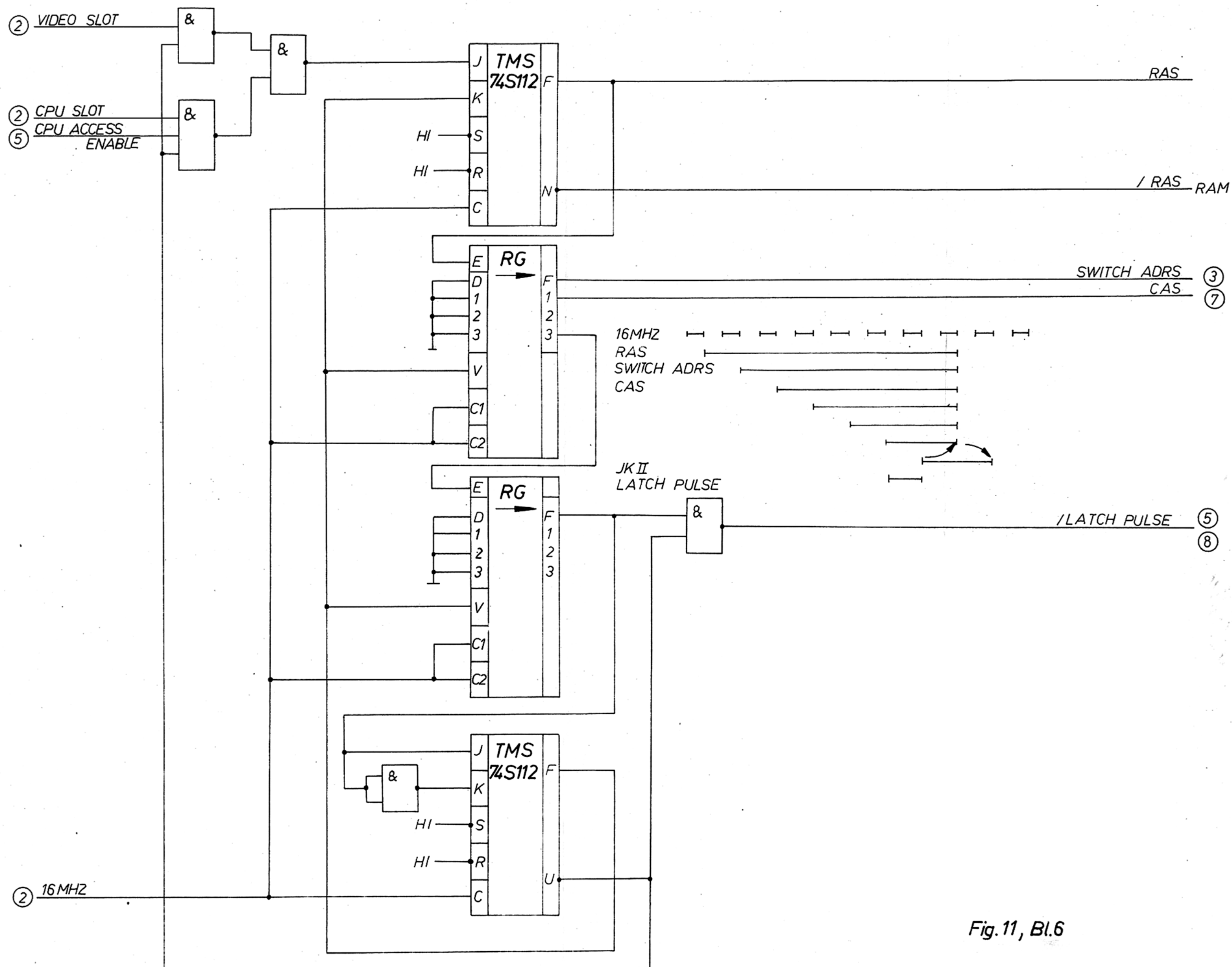
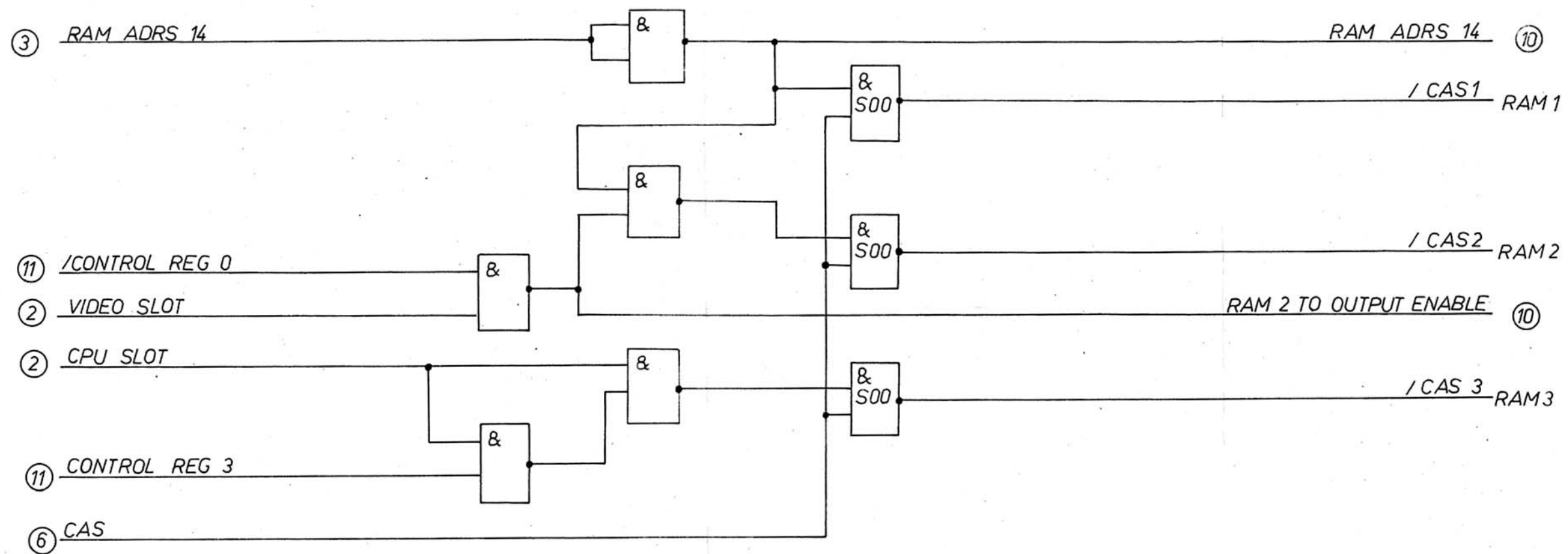


Fig.11, Bl.6



Detail RAM - Beschaltung

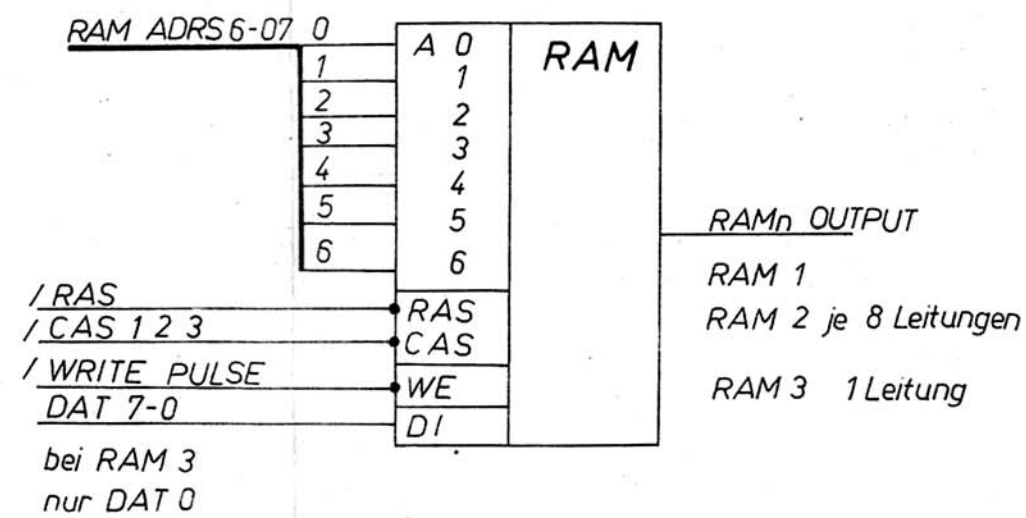


Fig. 11, Bl. 7

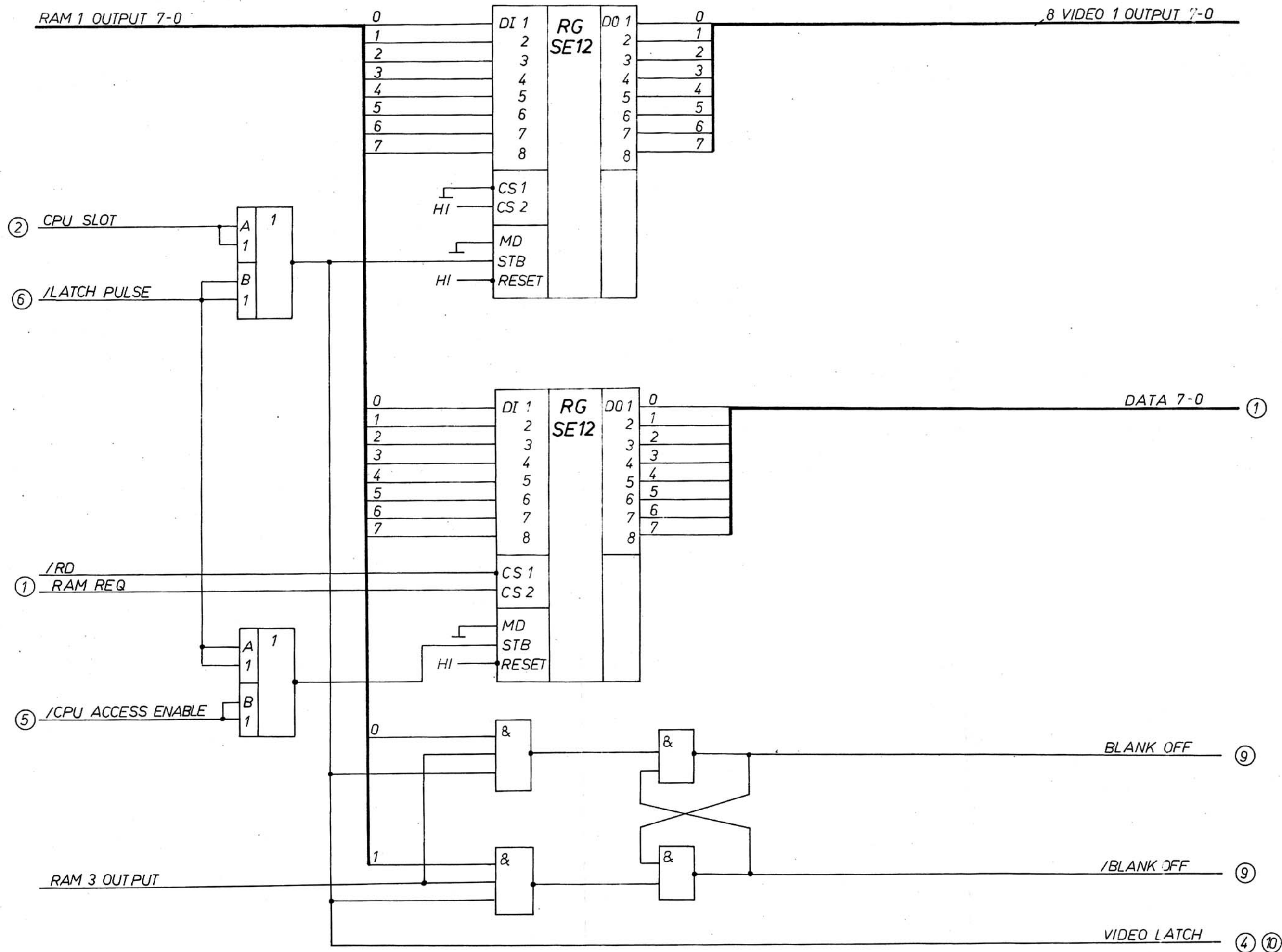
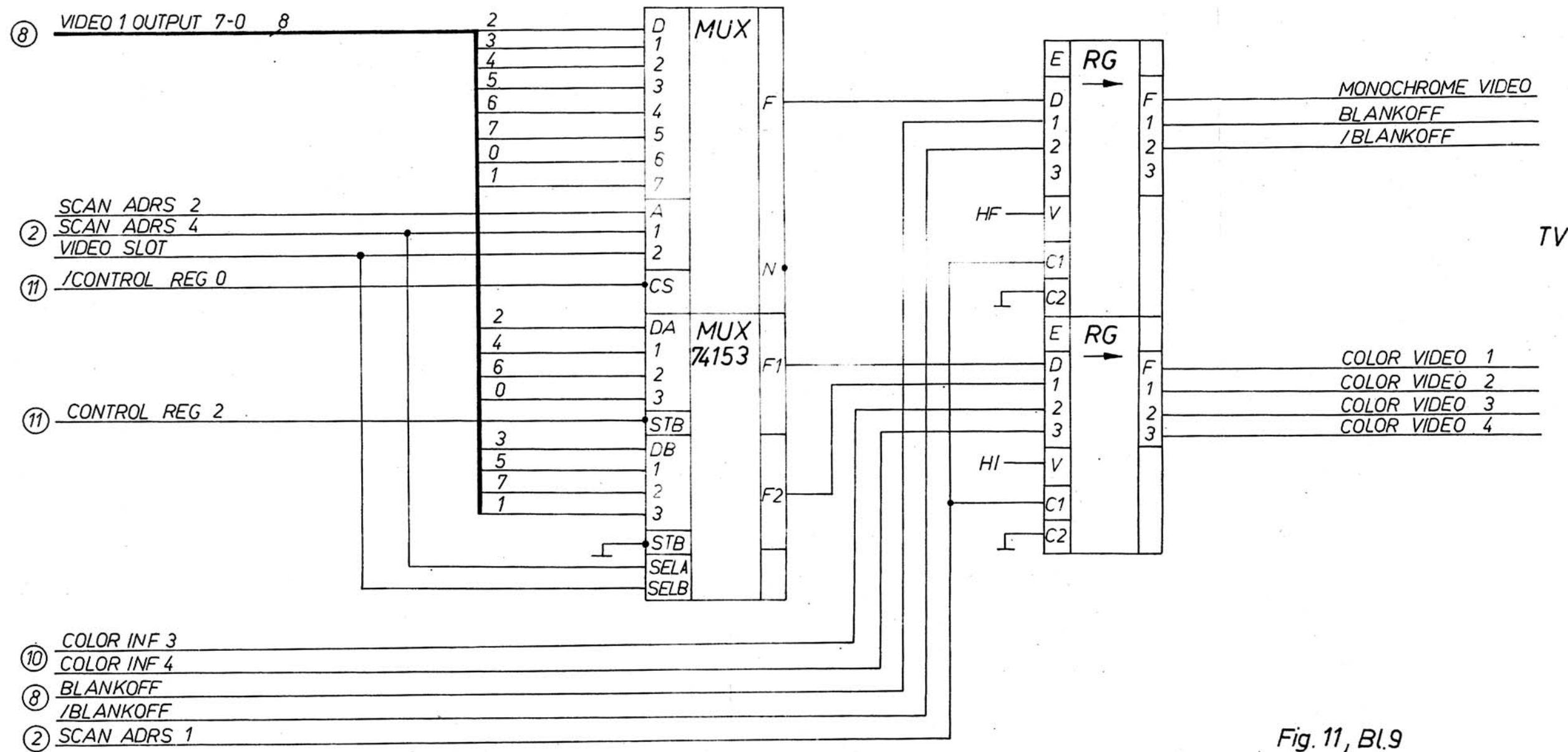
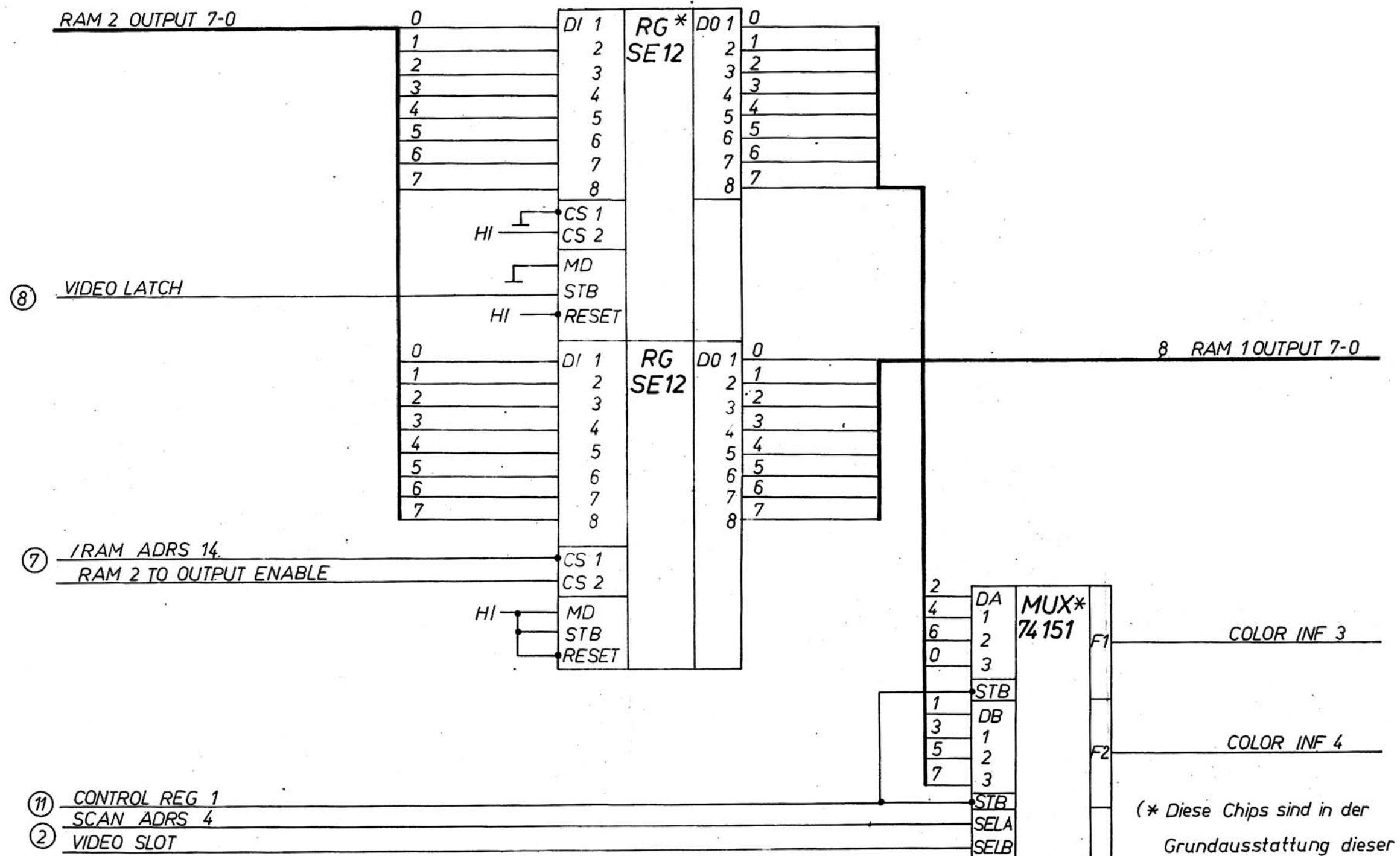


Fig.11, Bl.8





Ergänzungs - Platine 005. C / SE

Fig.11, Bl.10

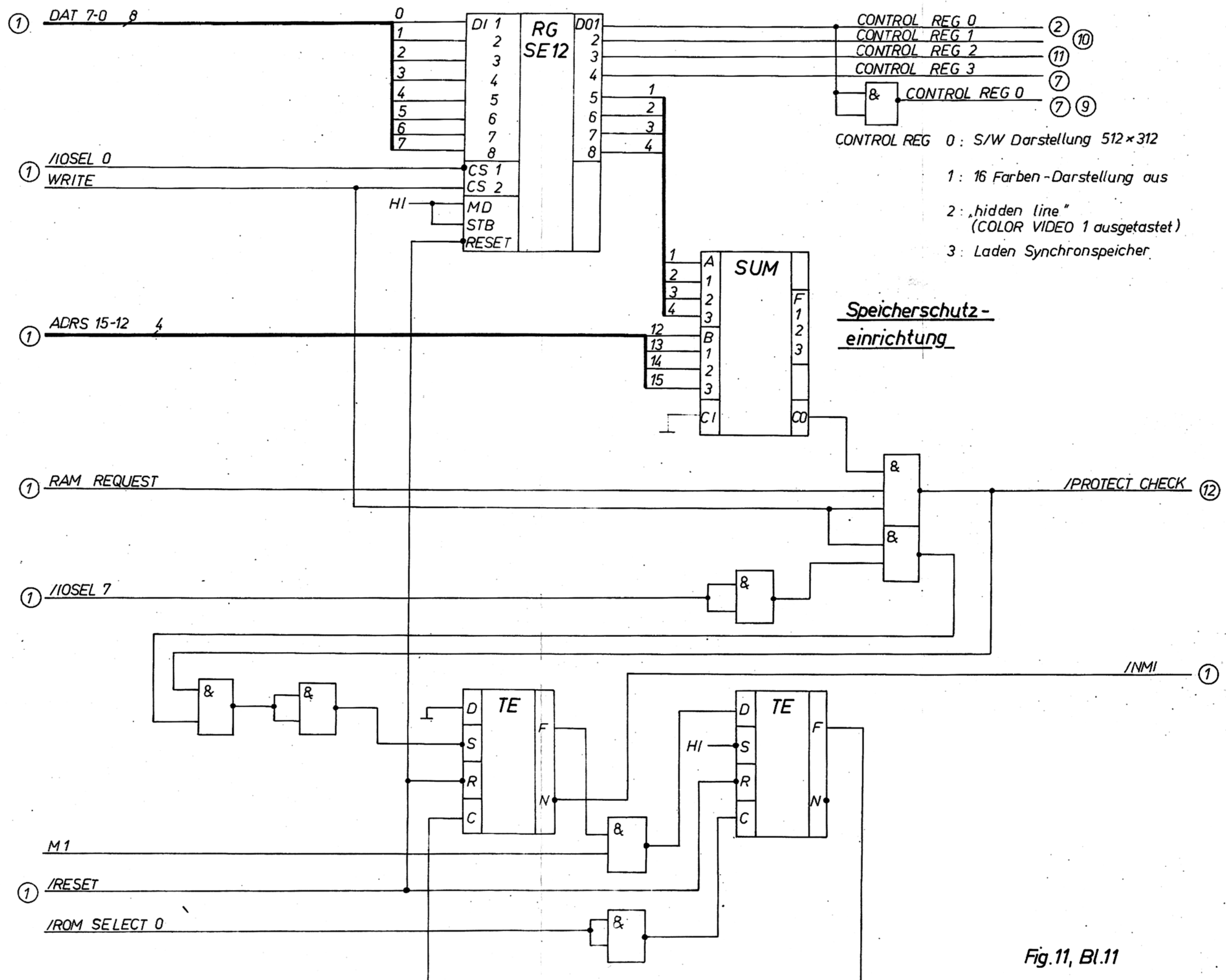


Fig.11, Bl.11

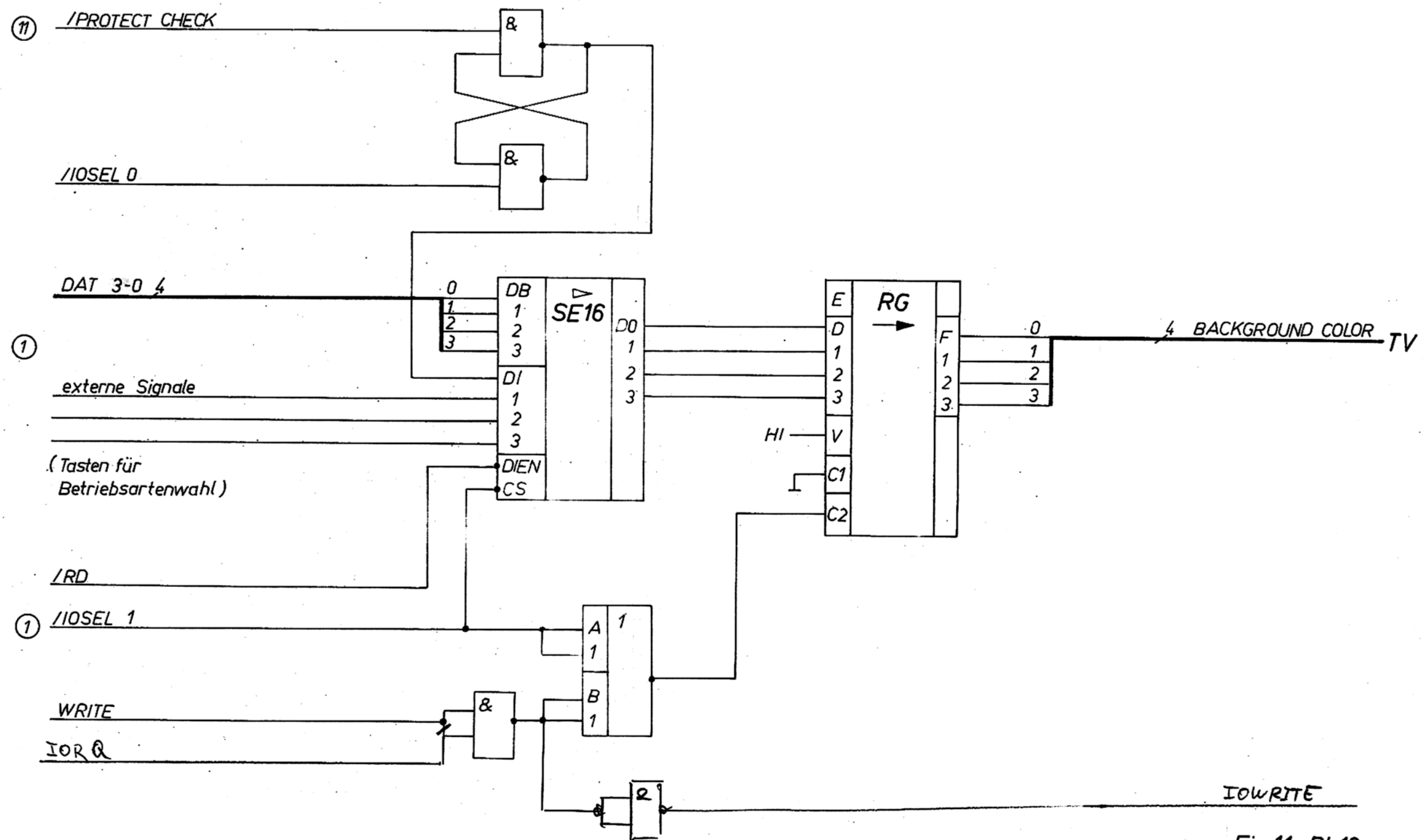


Fig. 11, Bl. 12

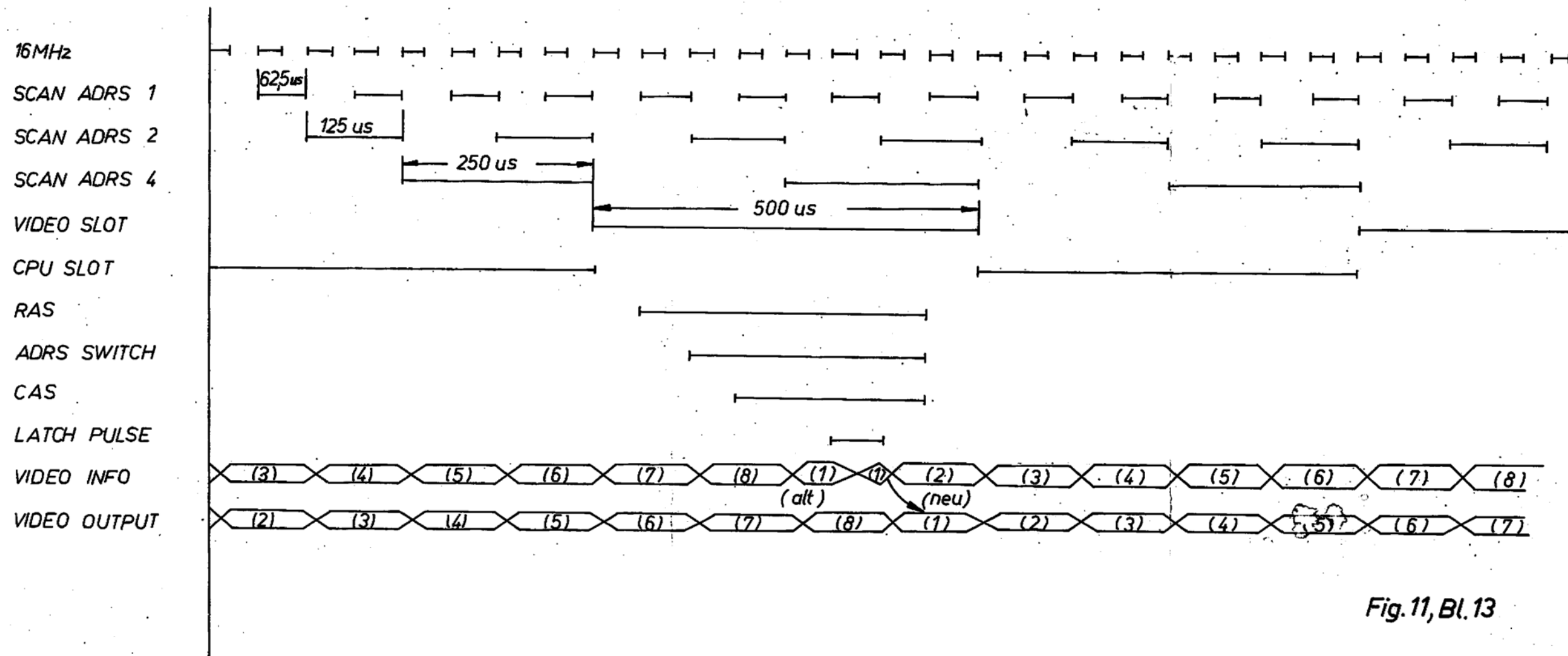


Fig.11, Bl.13